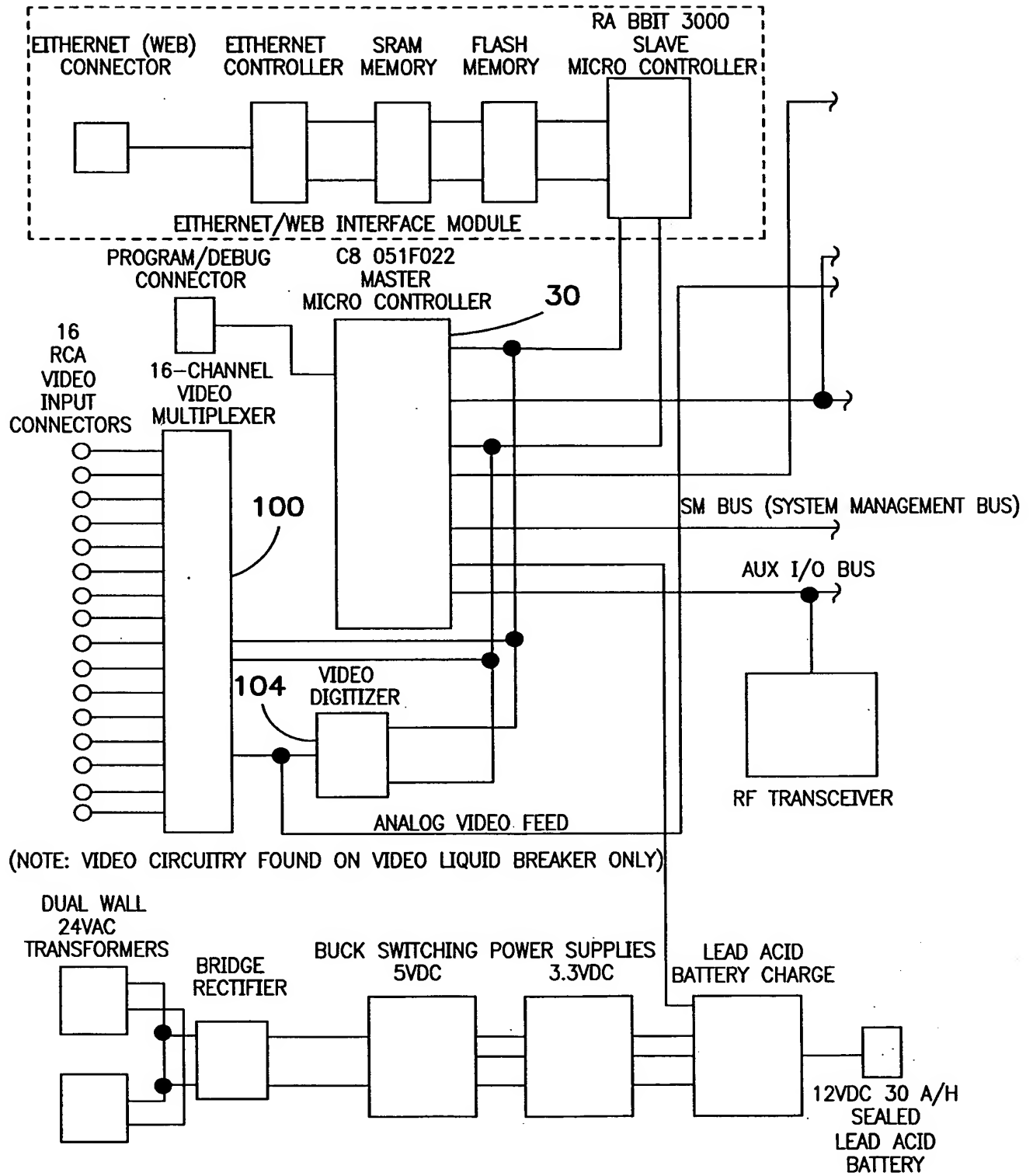
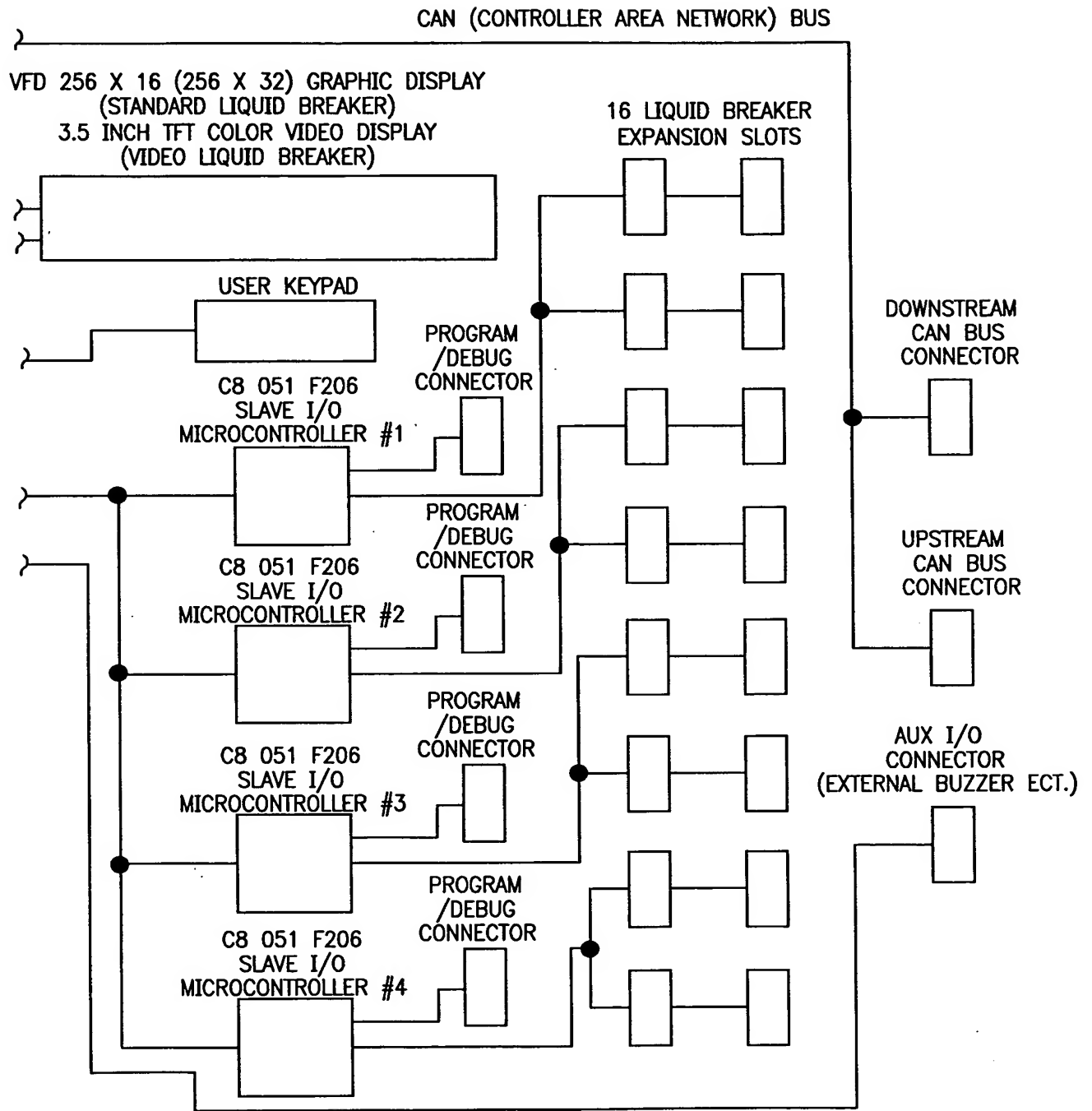


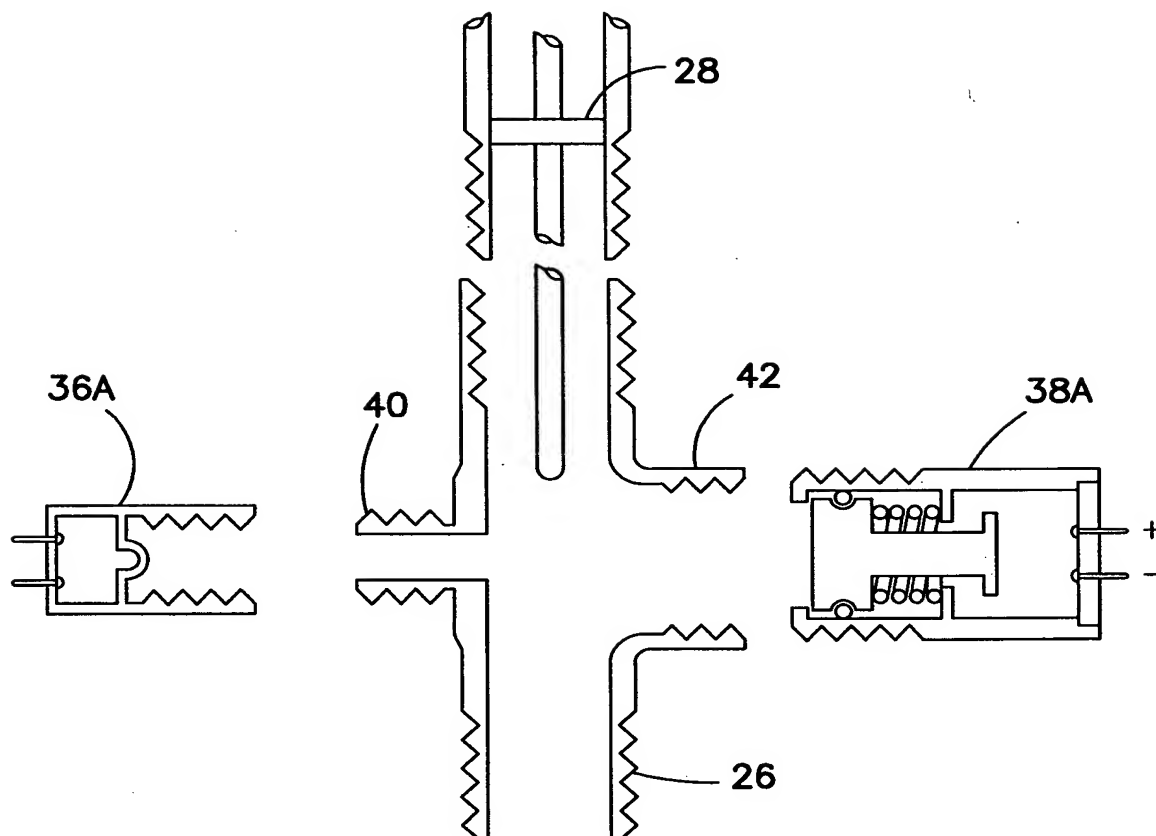
FIG. 1A



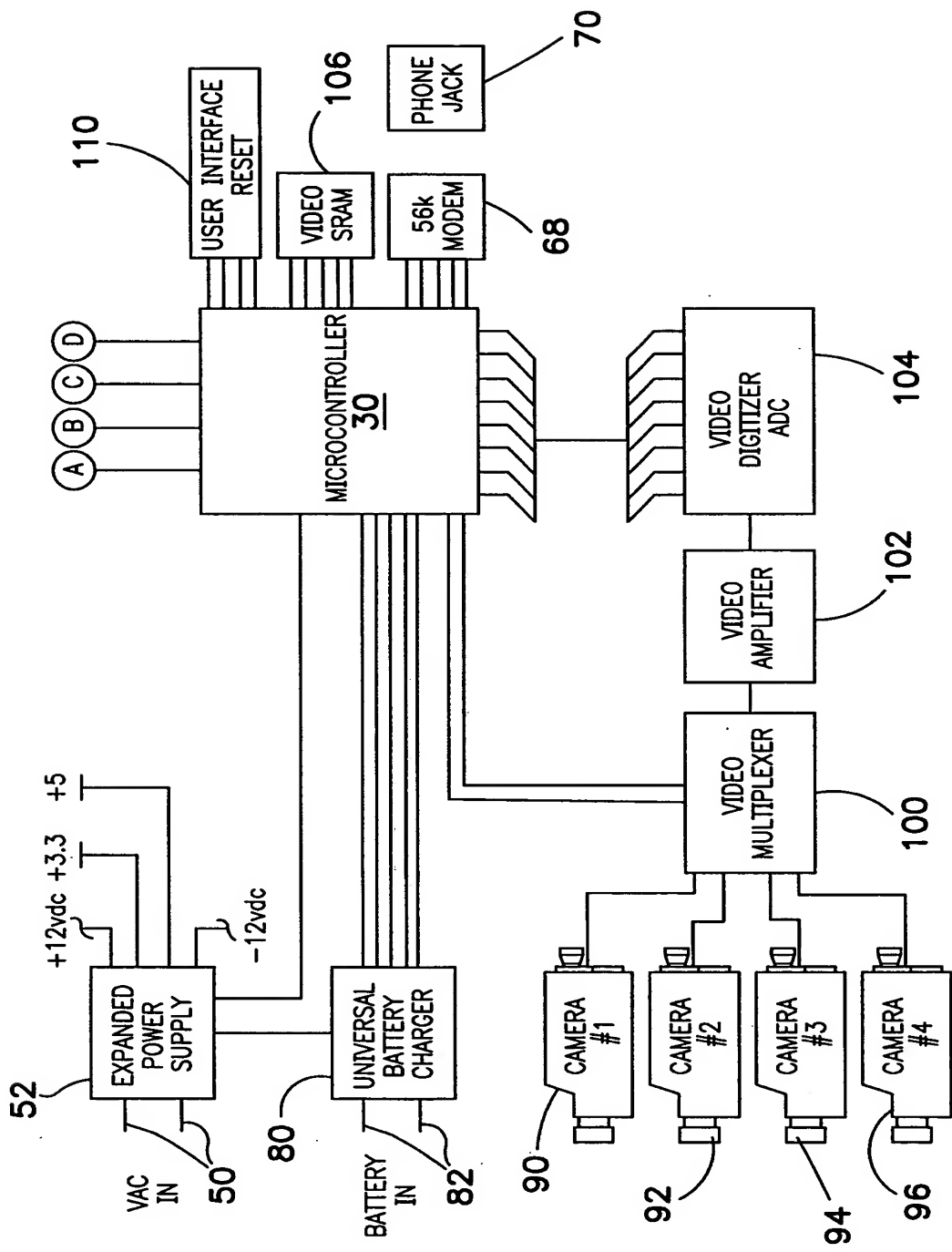
**FIG. 1B-1**



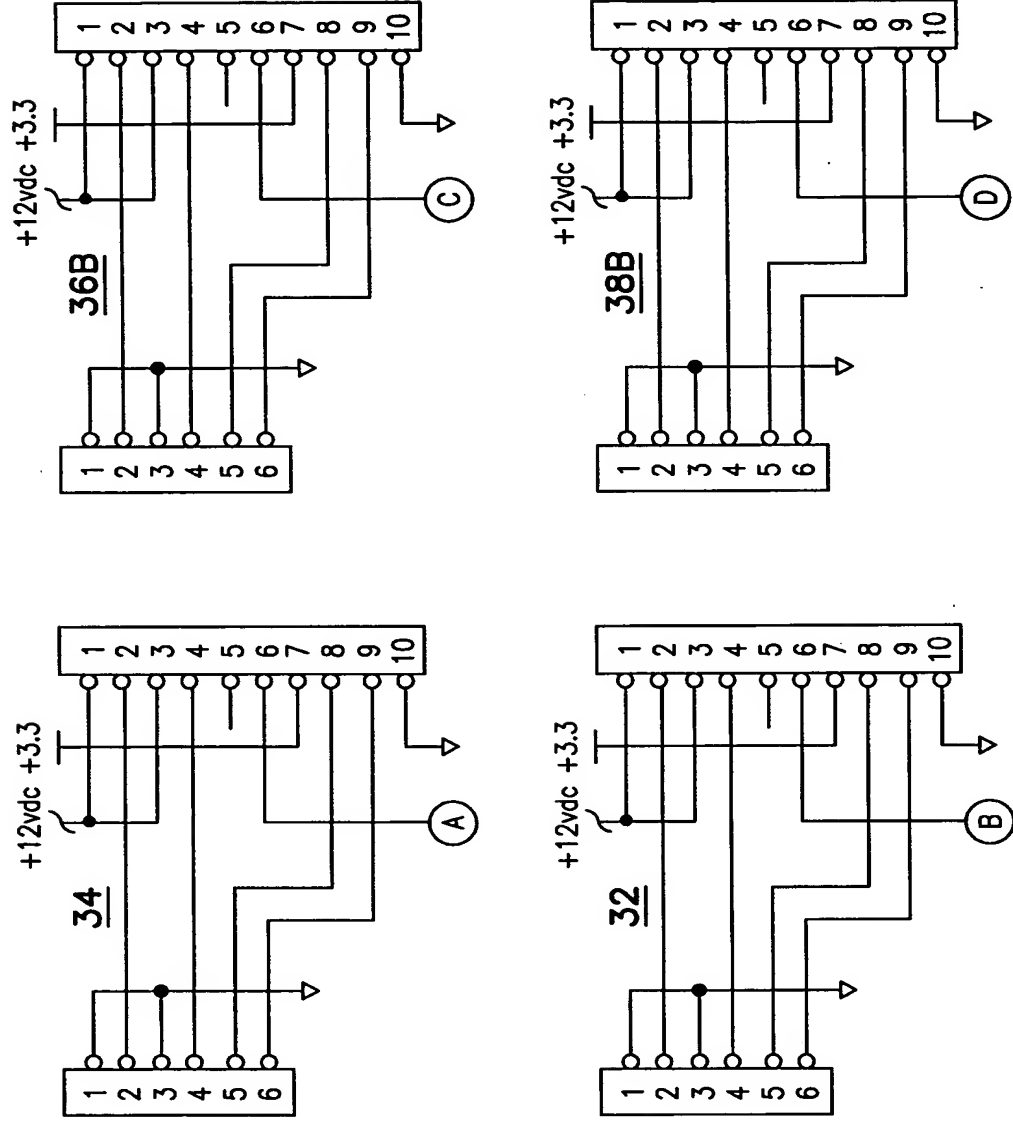
**FIG. 1B-2**



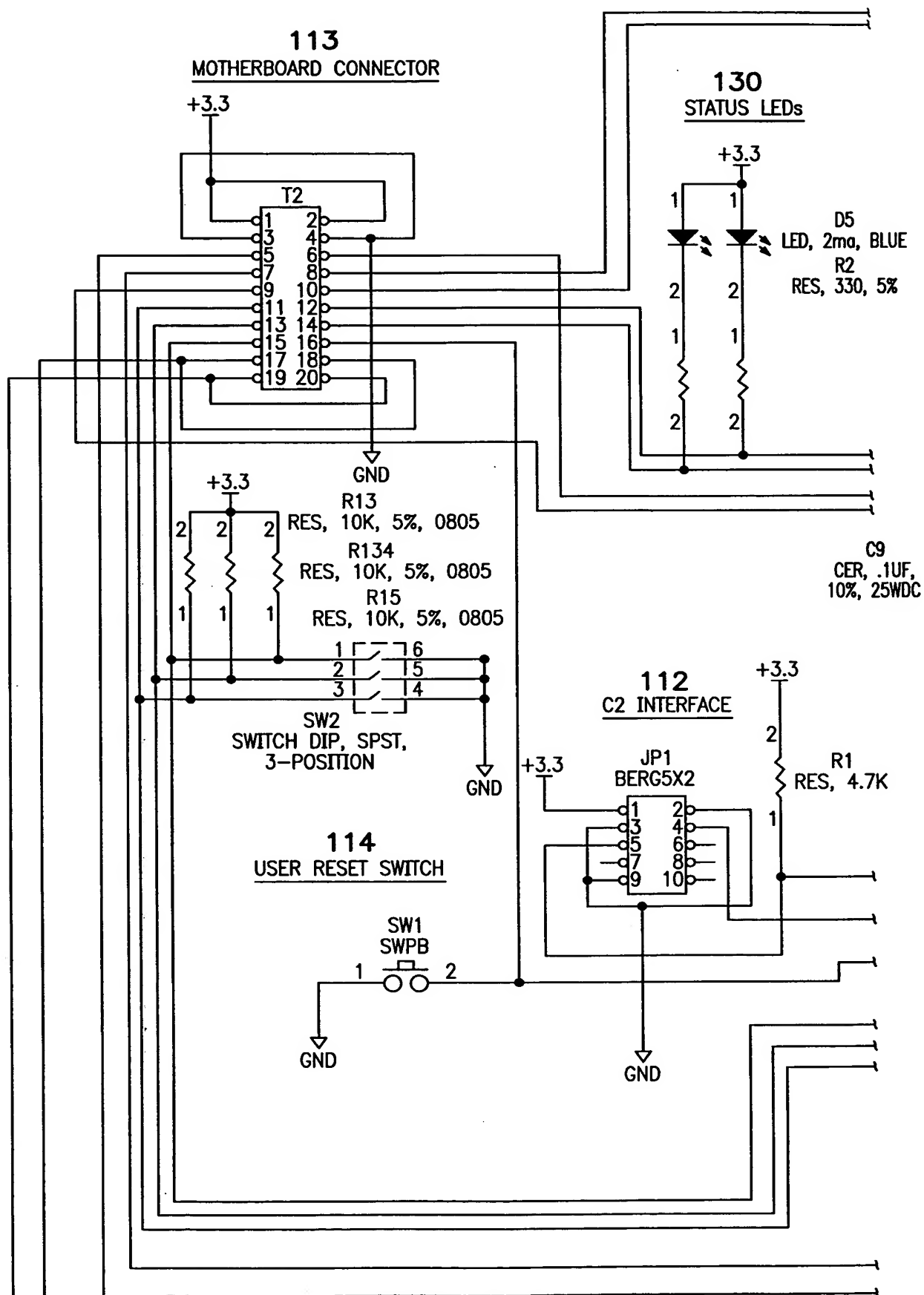
**FIG. 2**



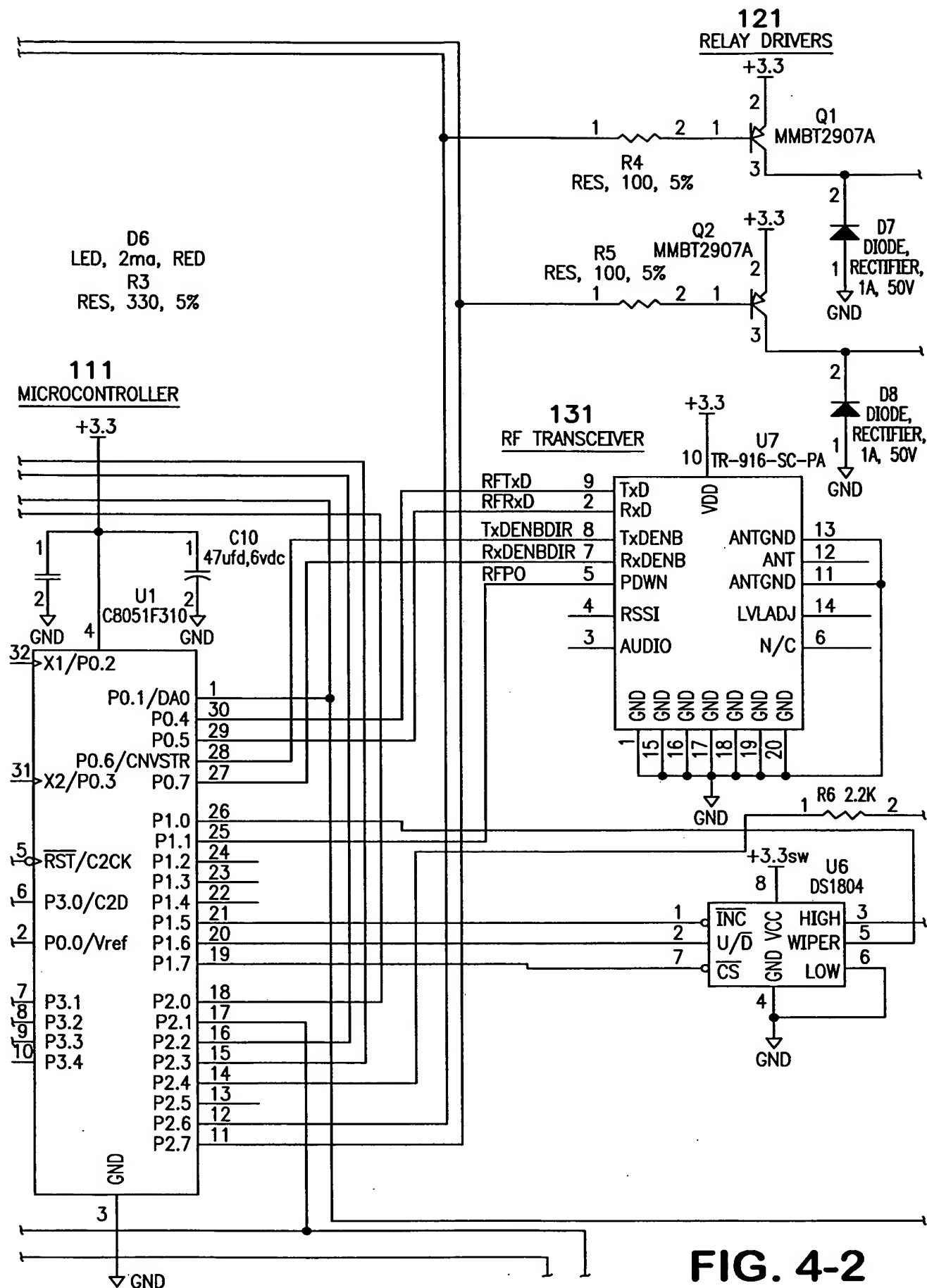
**FIG. 3A**



**FIG. 3B**

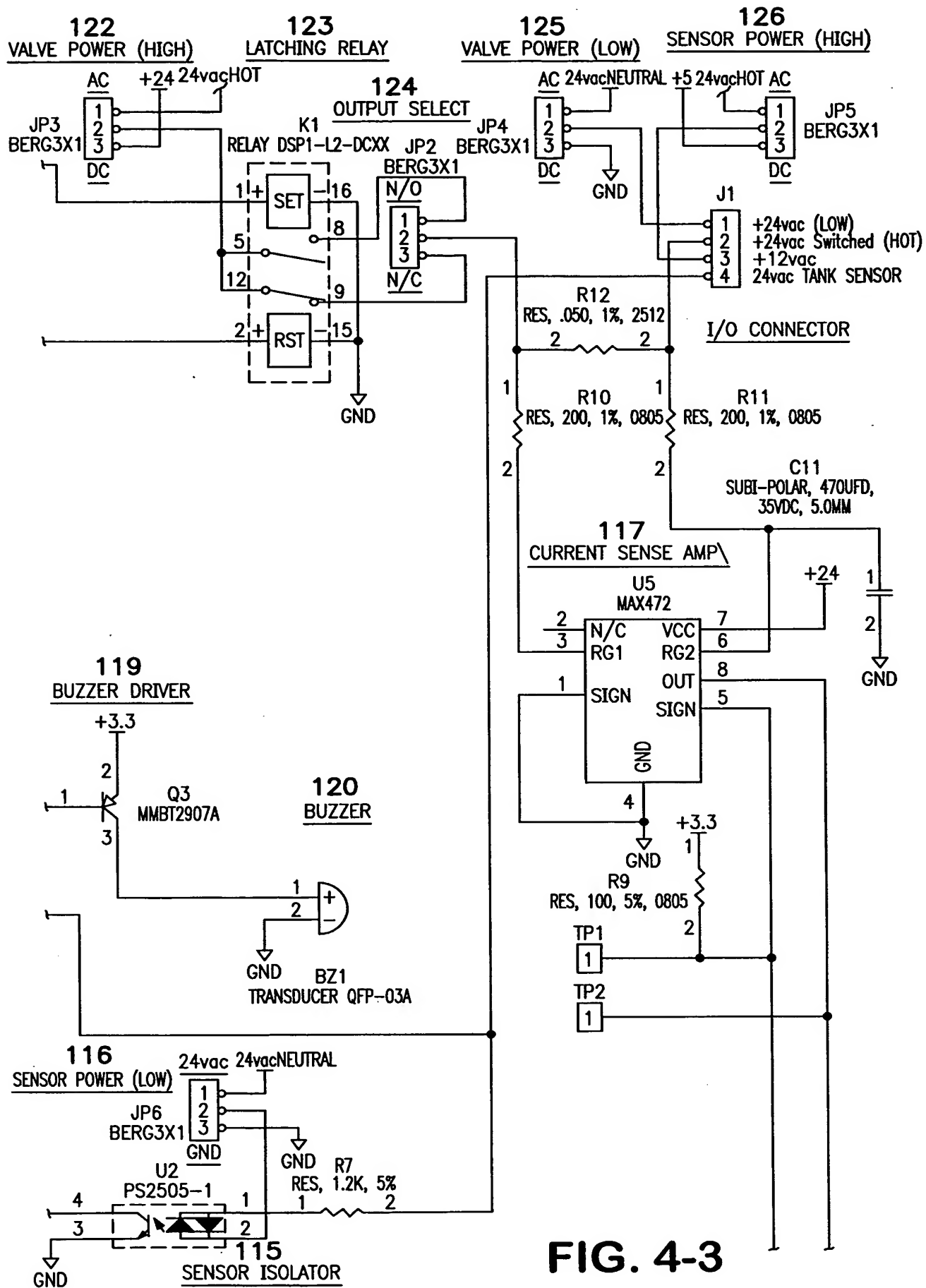


**FIG. 4-1**

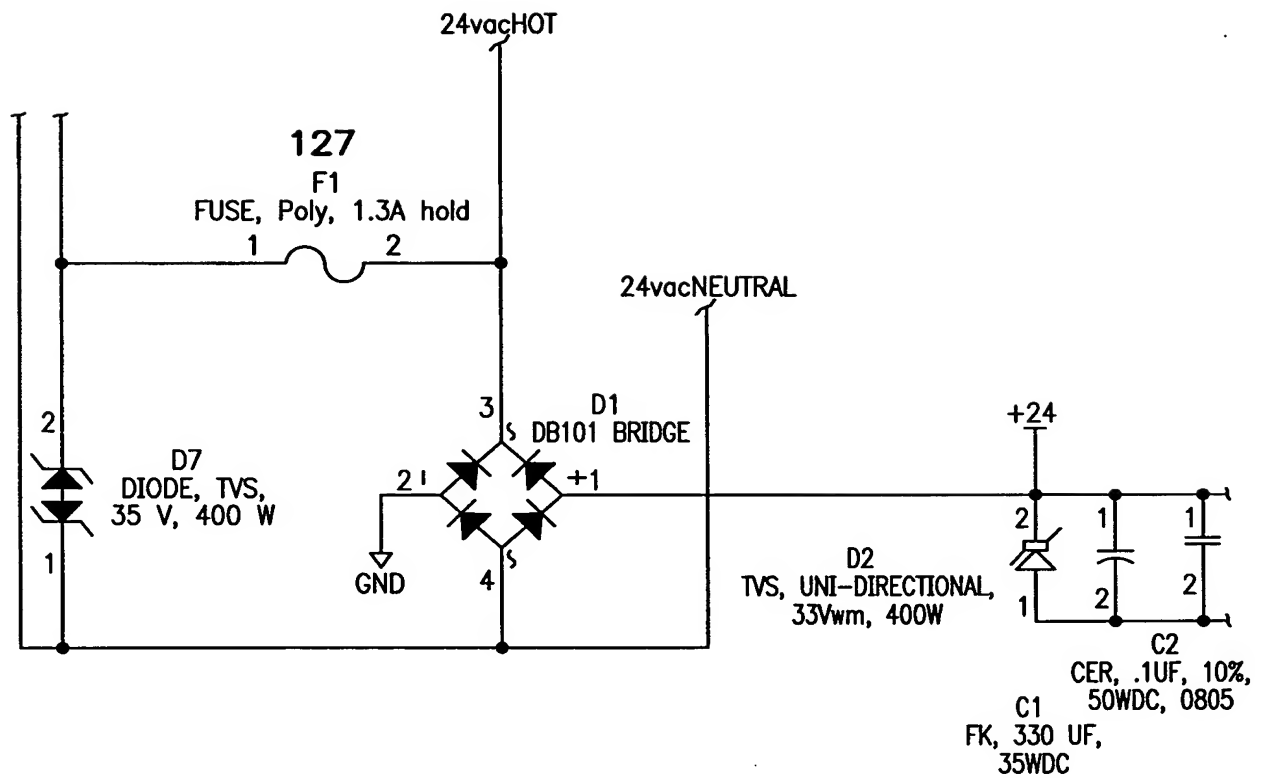


**FIG. 4-2**

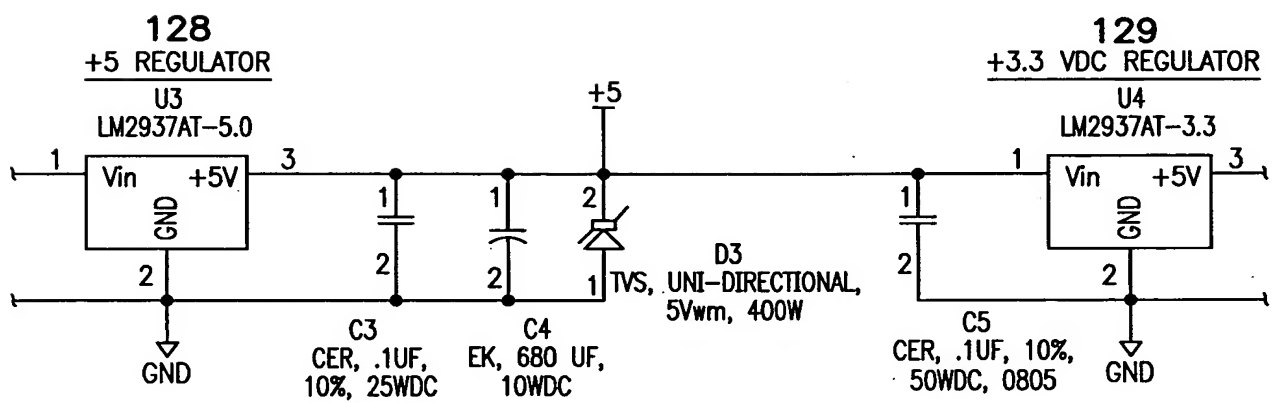




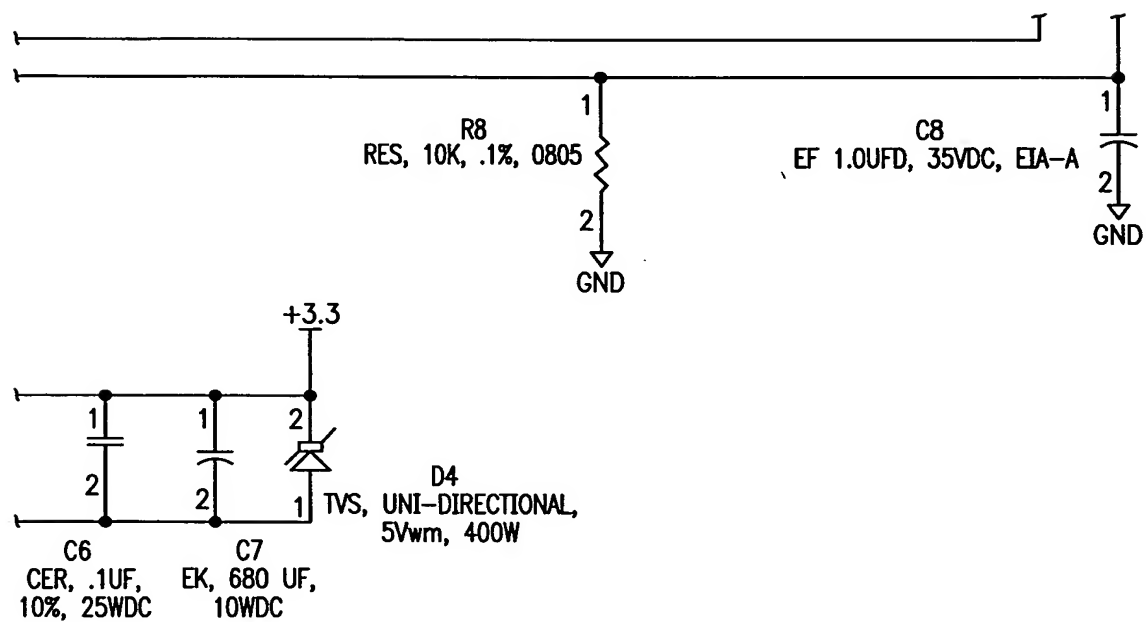
**FIG. 4-3**



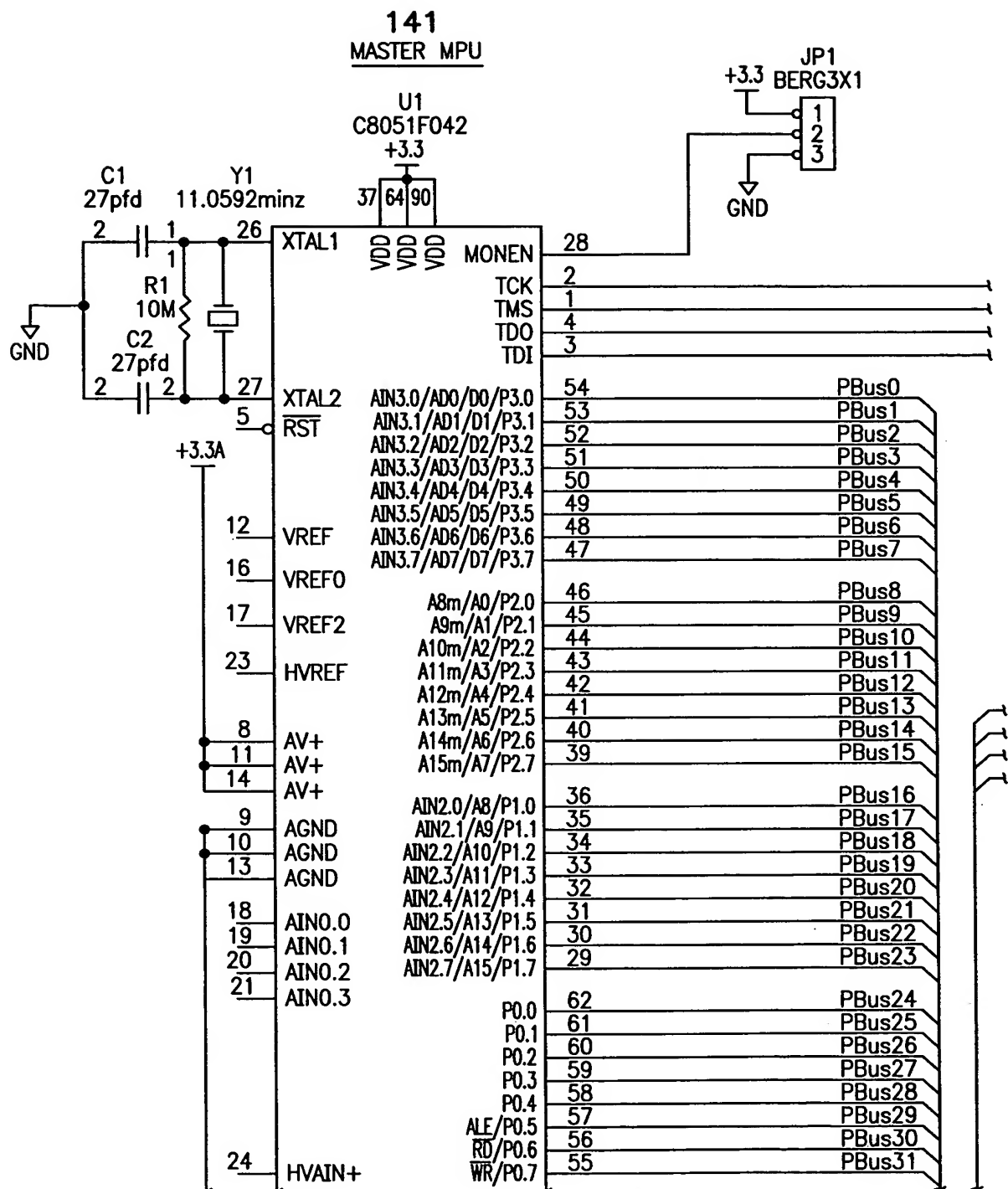
**FIG. 4-4**



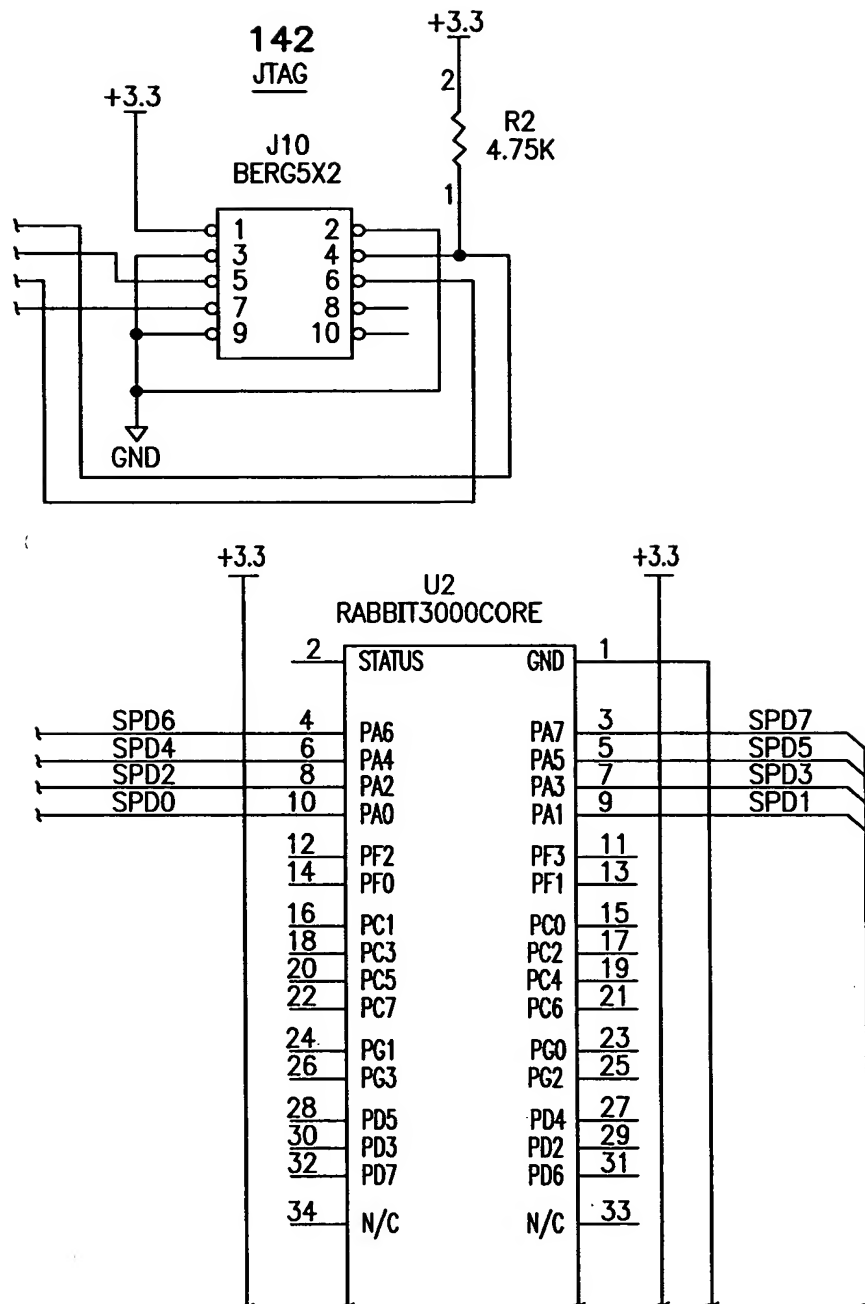
**FIG. 4-5**



**FIG. 4-6**

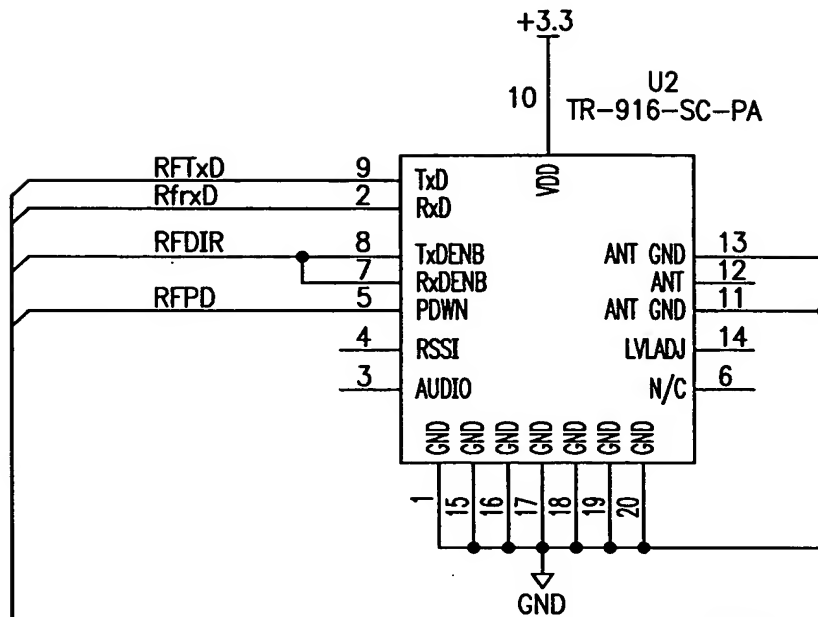


**FIG. 5-1**

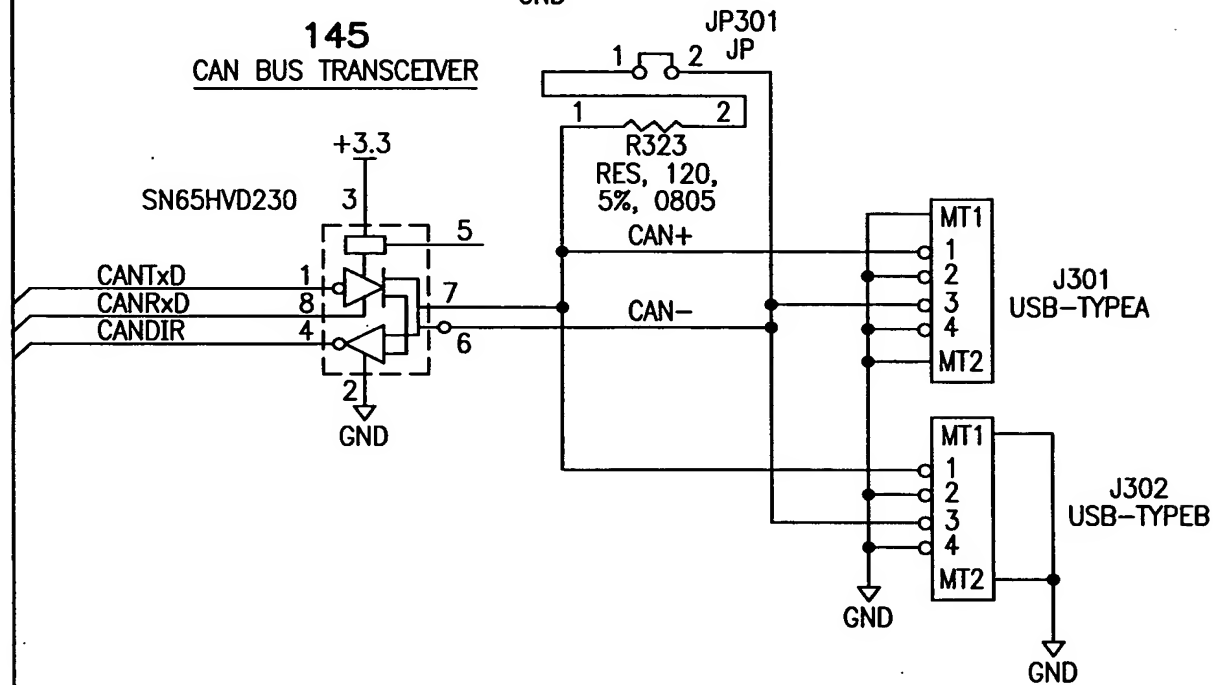


**FIG. 5-2**

**144**  
**RF TRANSCEIVER**



**145**  
**CAN BUS TRANSCEIVER**

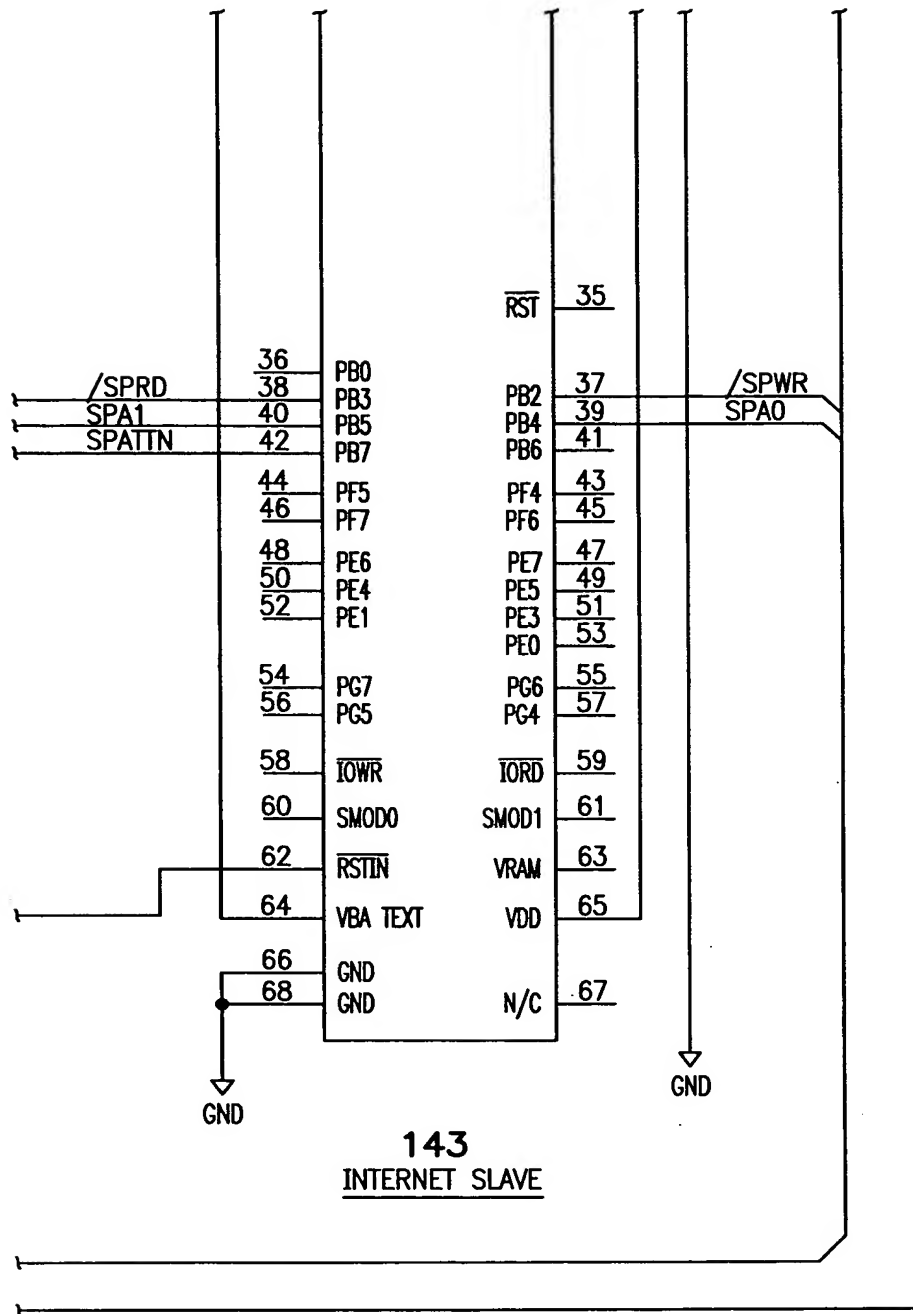


**FIG. 5-3**

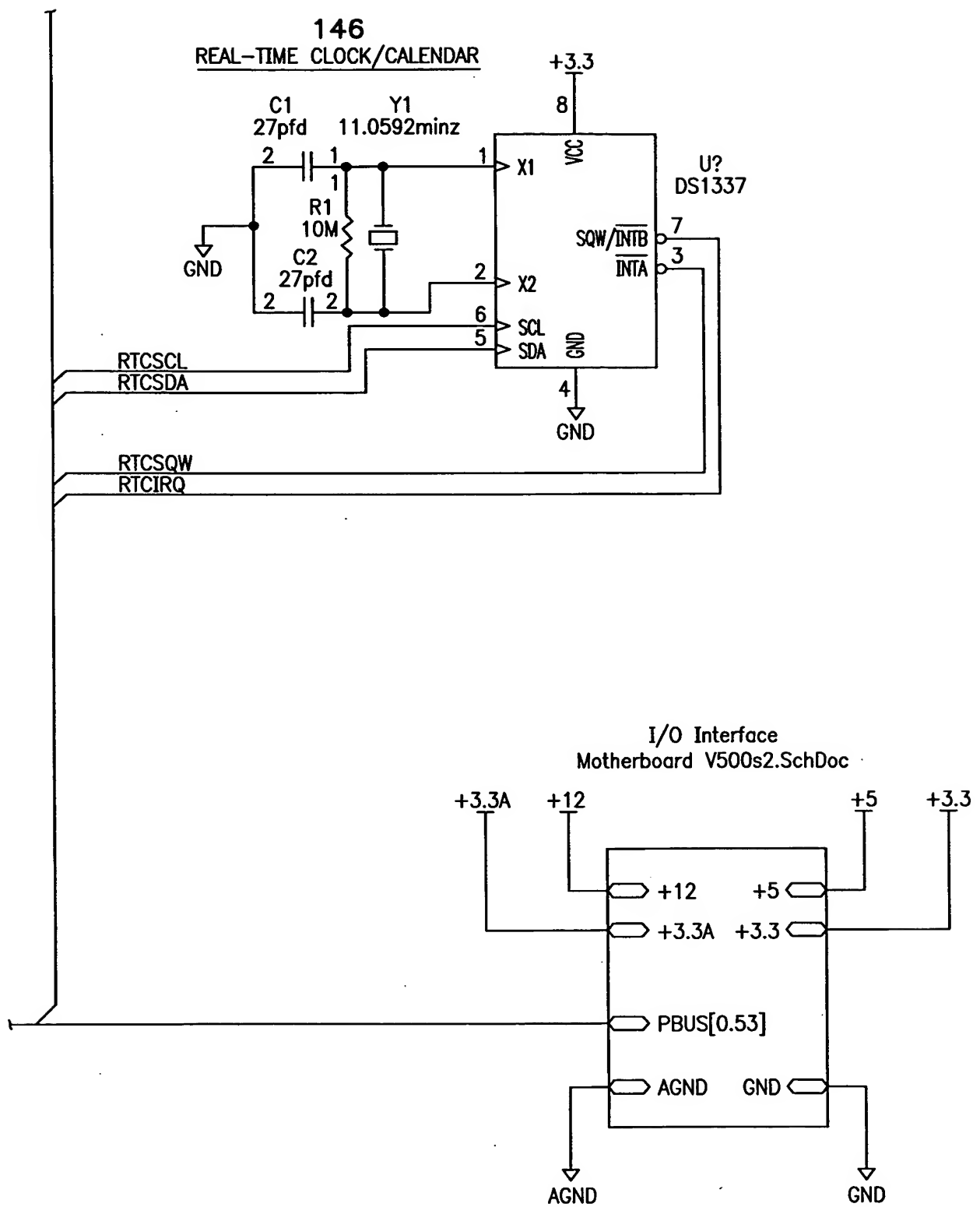


**FIG. 5-4**





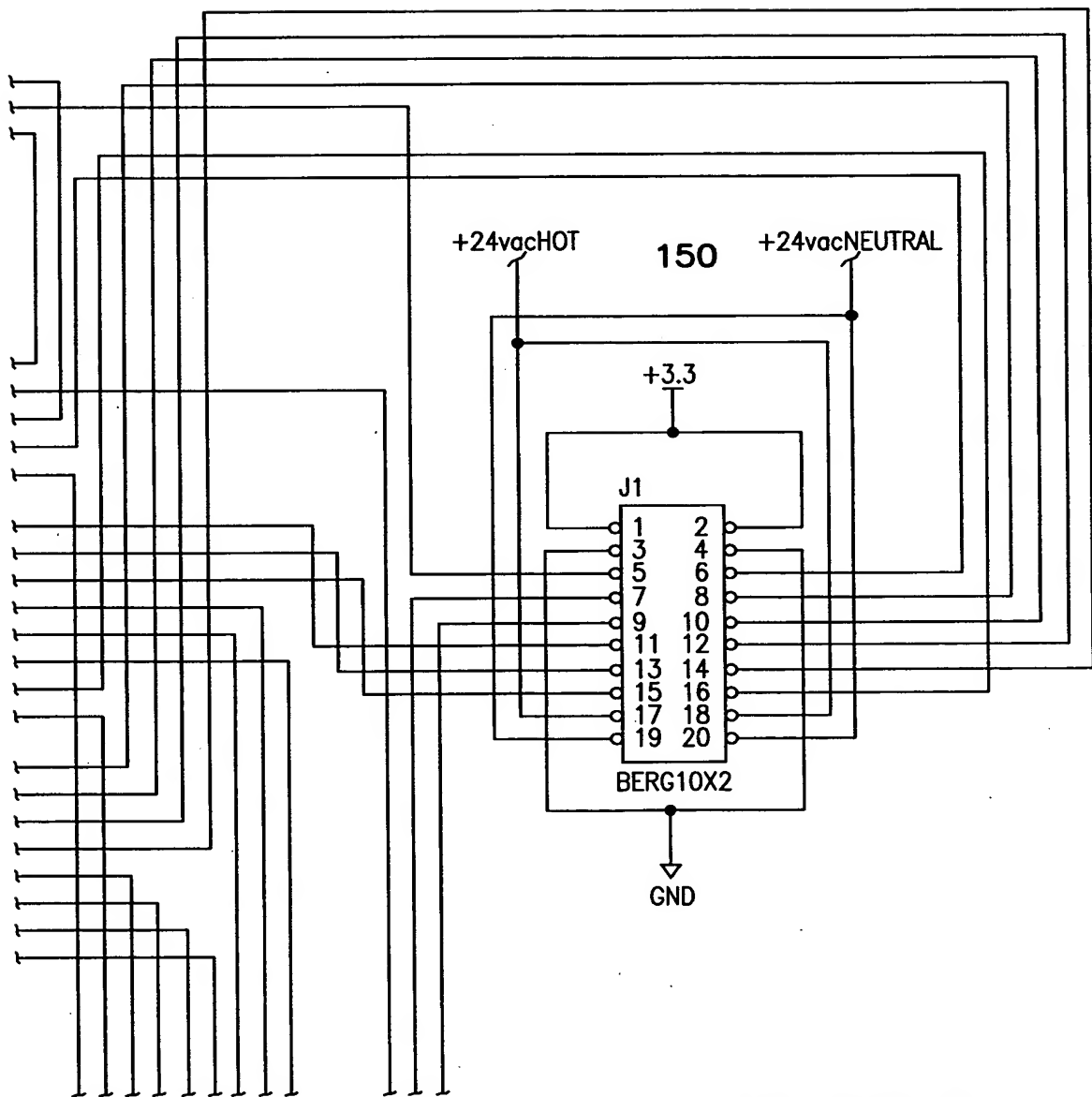
**FIG. 5-5**



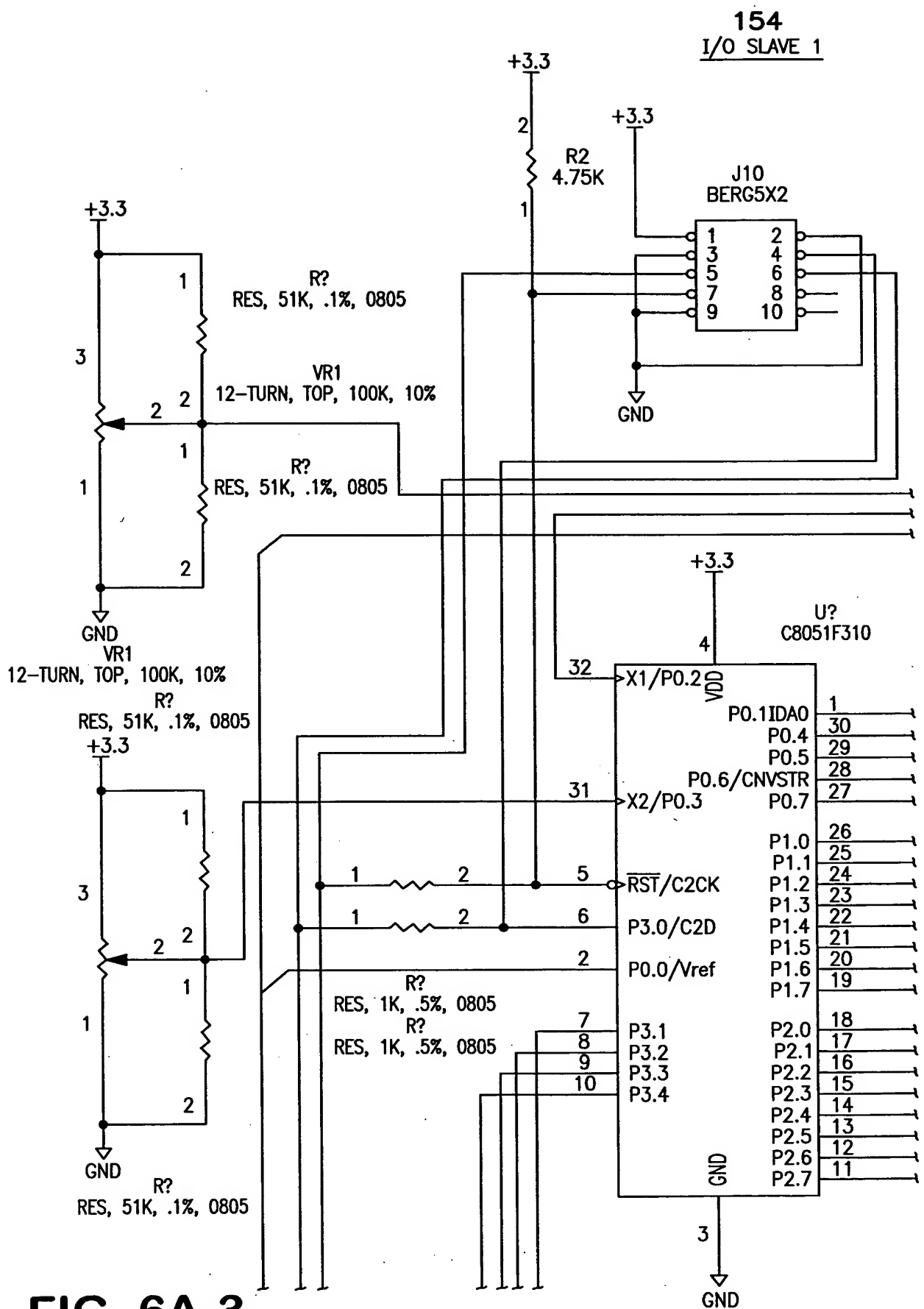
**FIG. 5-6**

I/O SLAVE 0



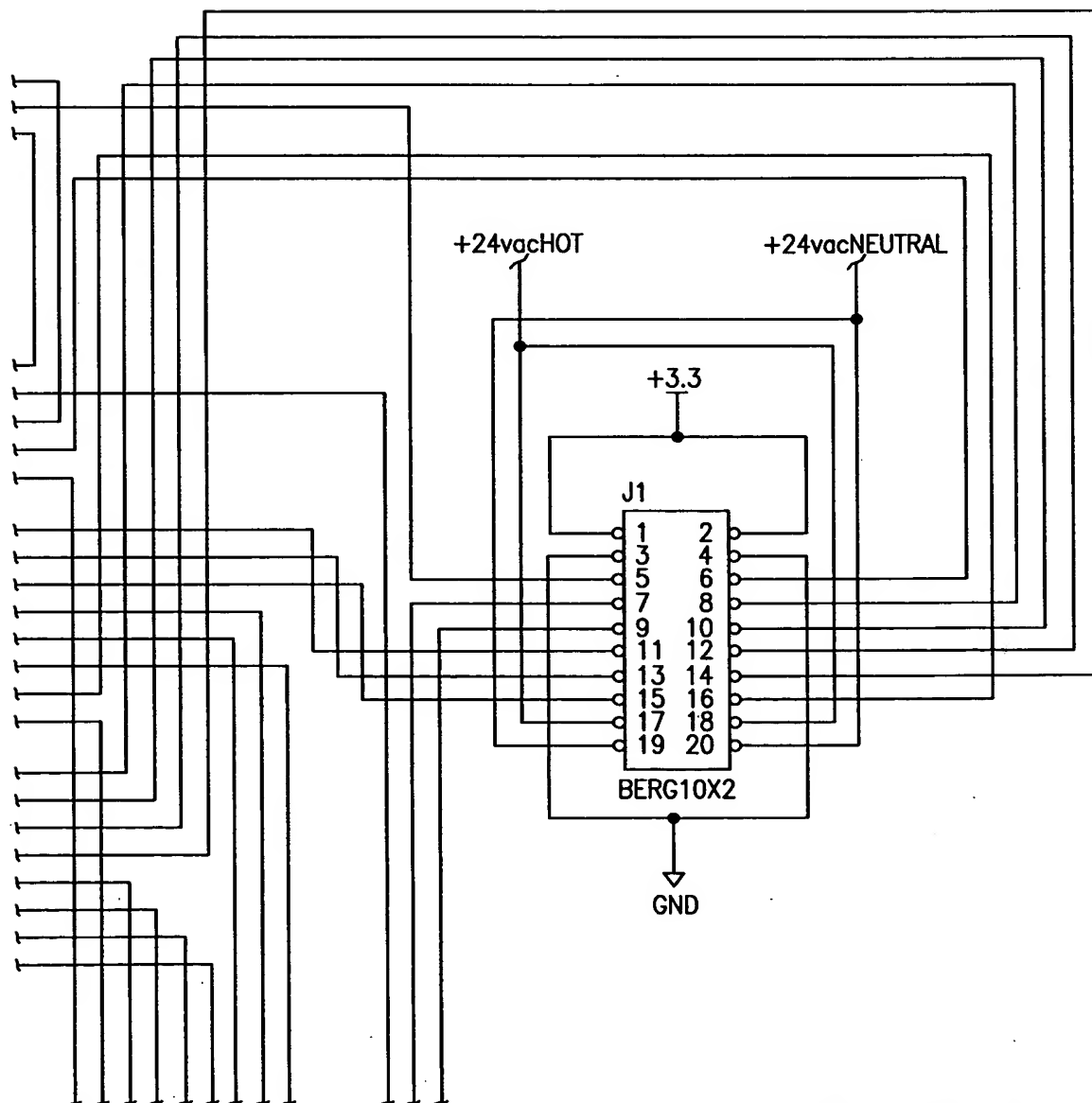


**FIG. 6A-2**

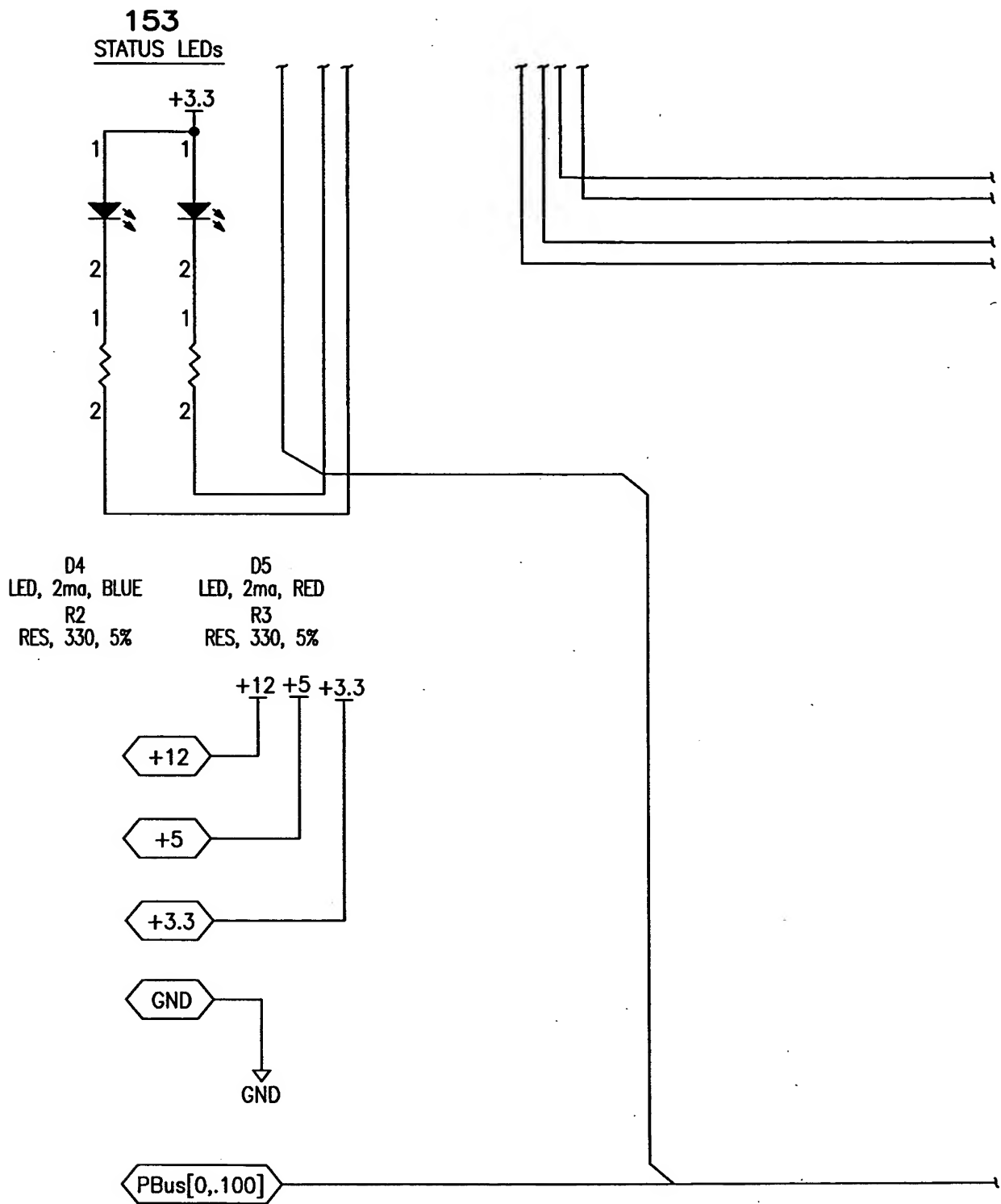


**FIG. 6A-3**

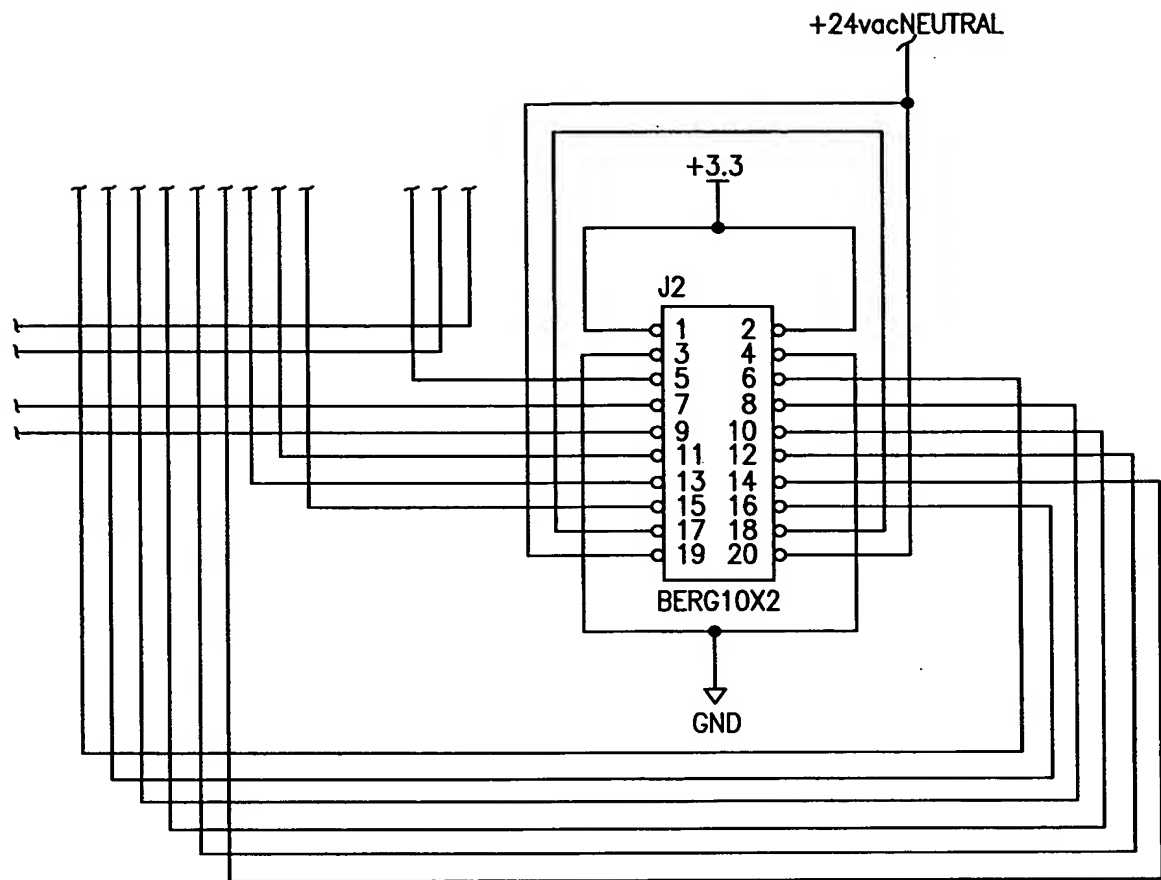
C2 INTERFACE



**FIG. 6A-4**

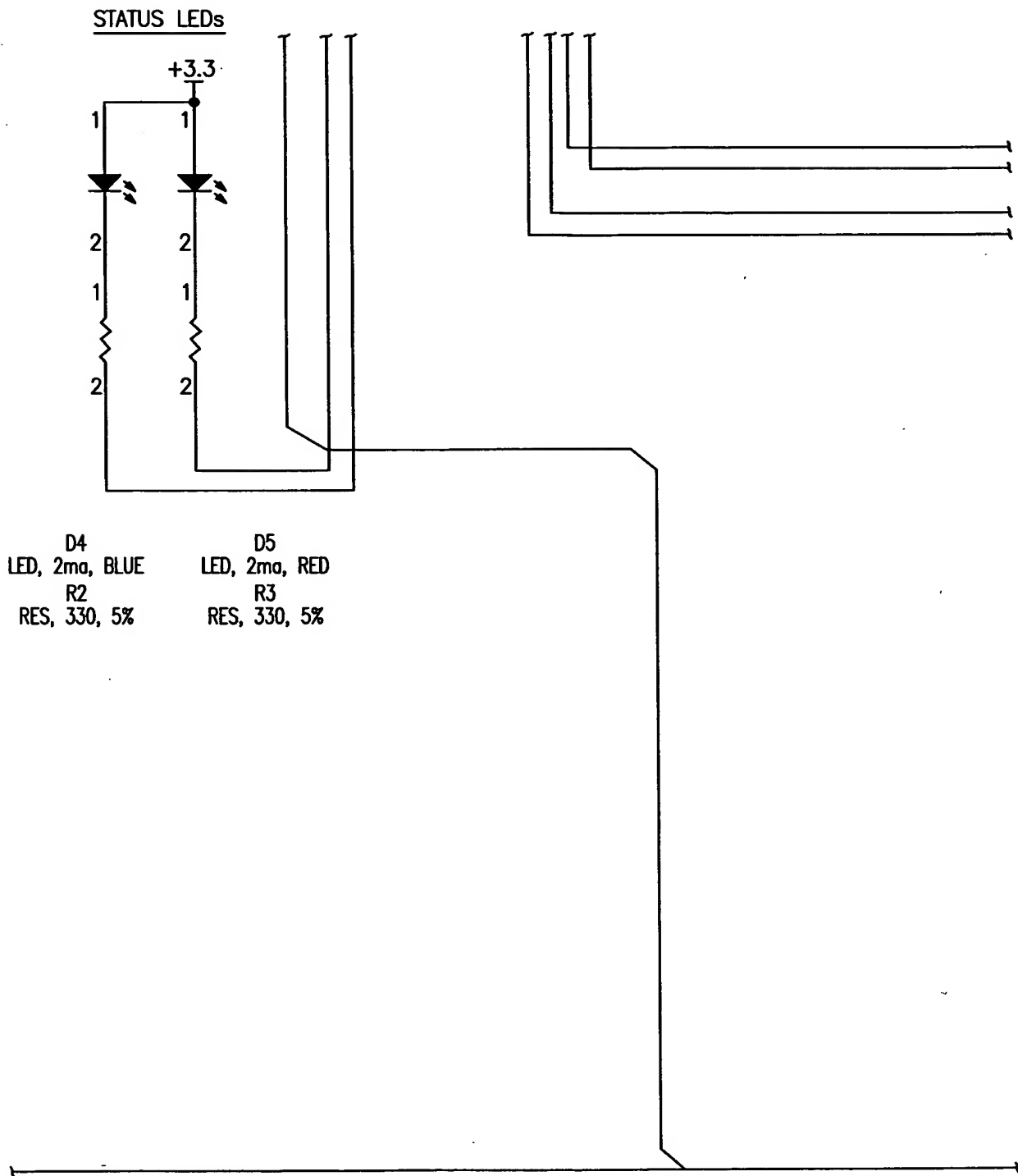


**FIG. 6A-5**

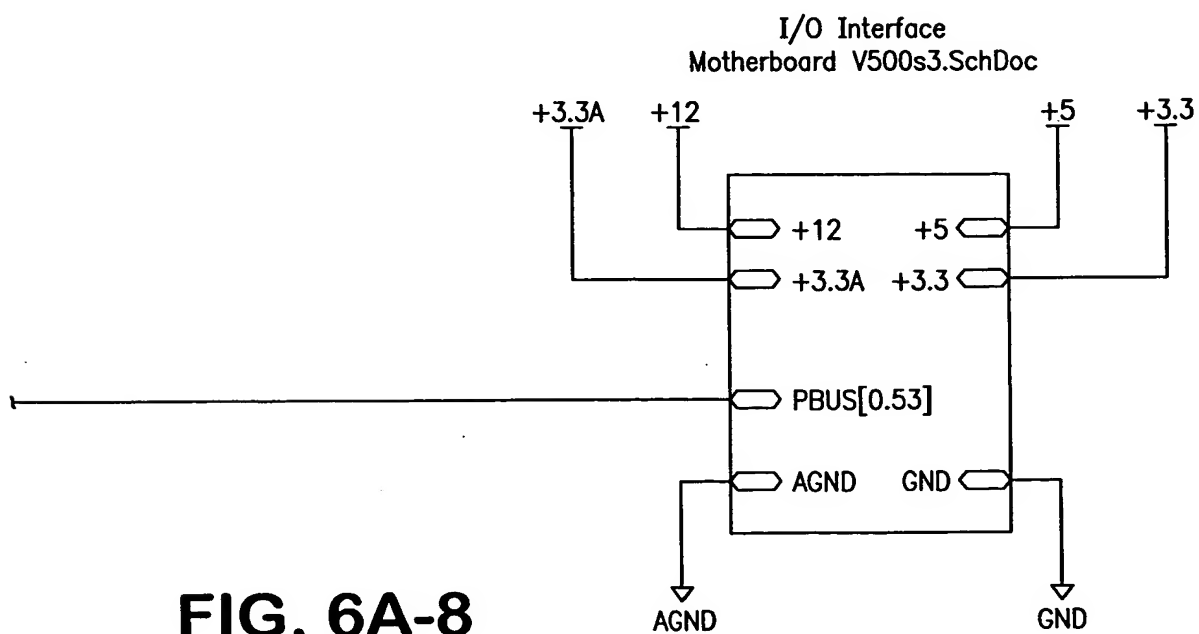
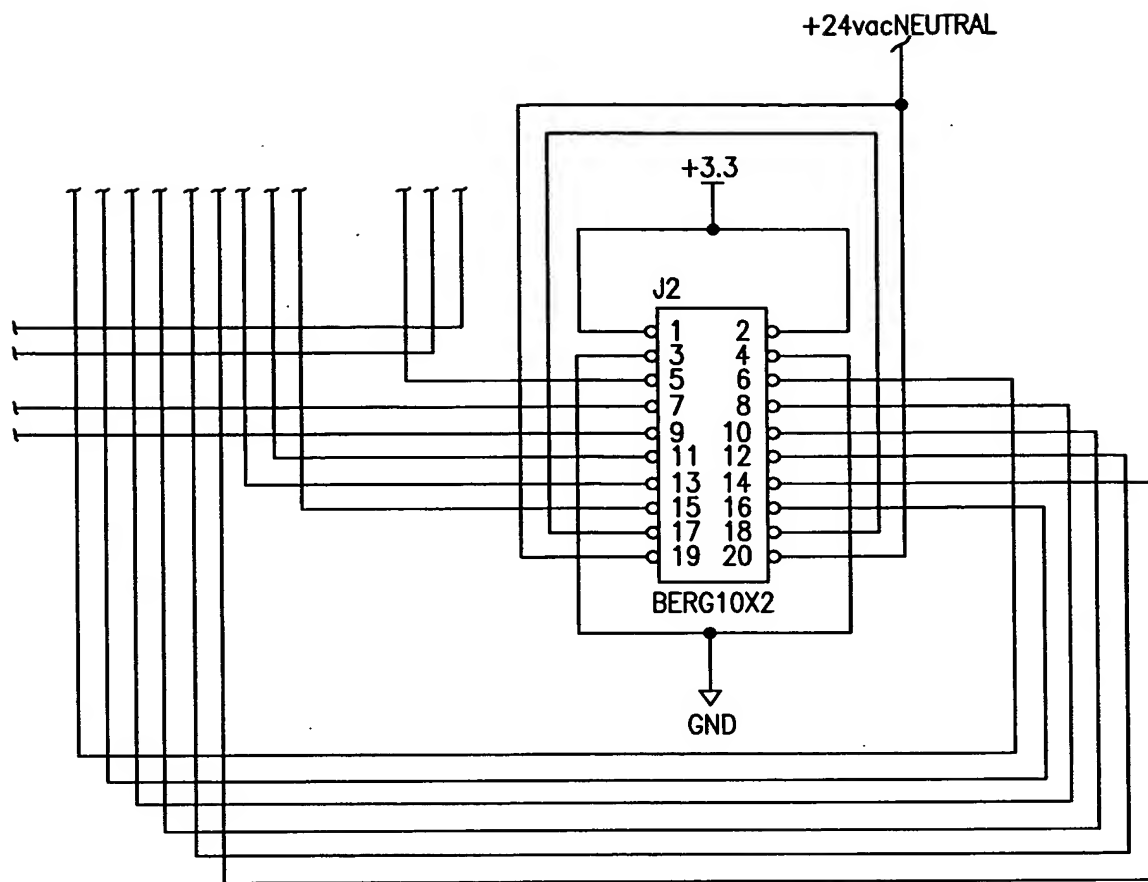


**FIG. 6A-6**

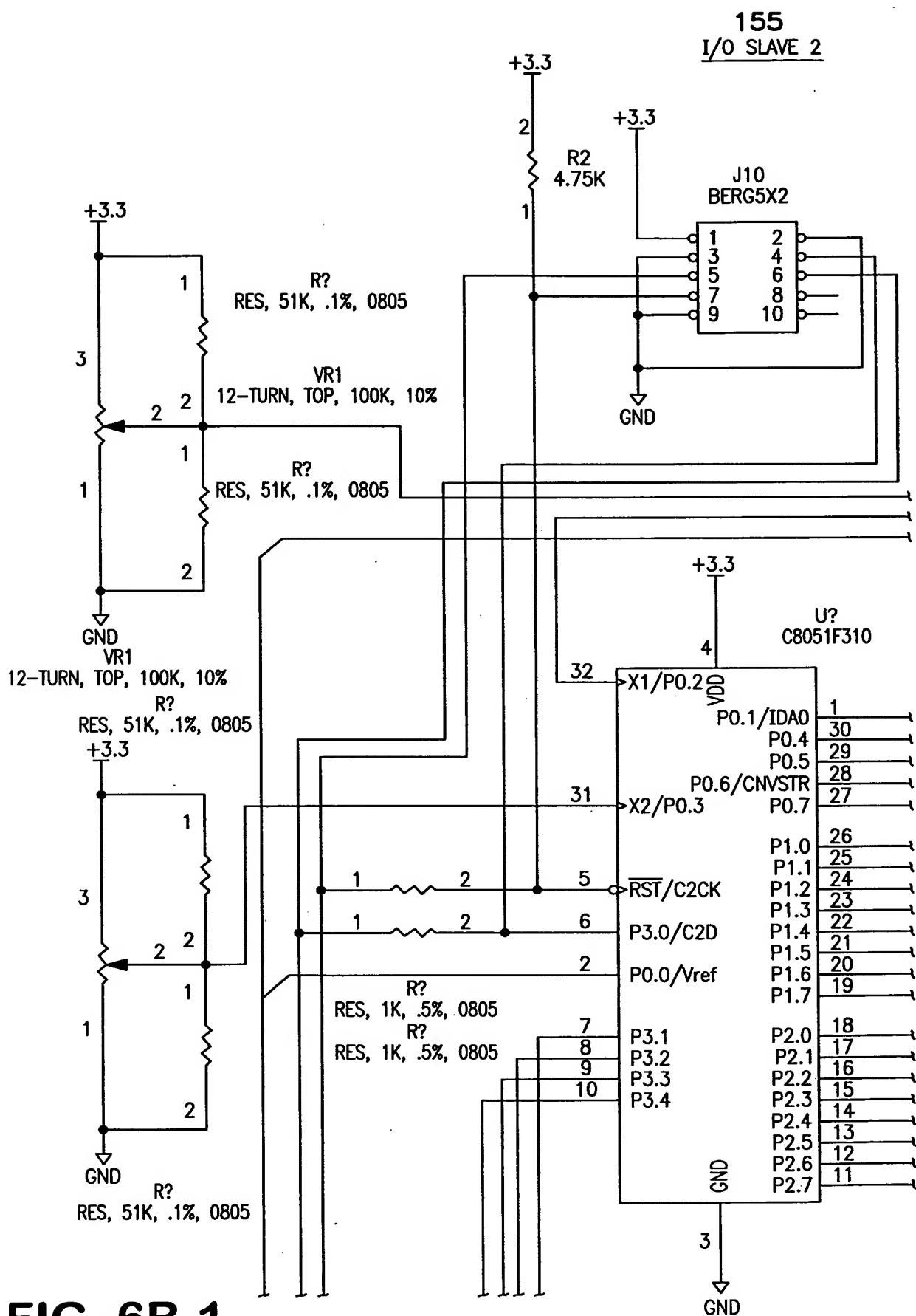




**FIG. 6A-7**

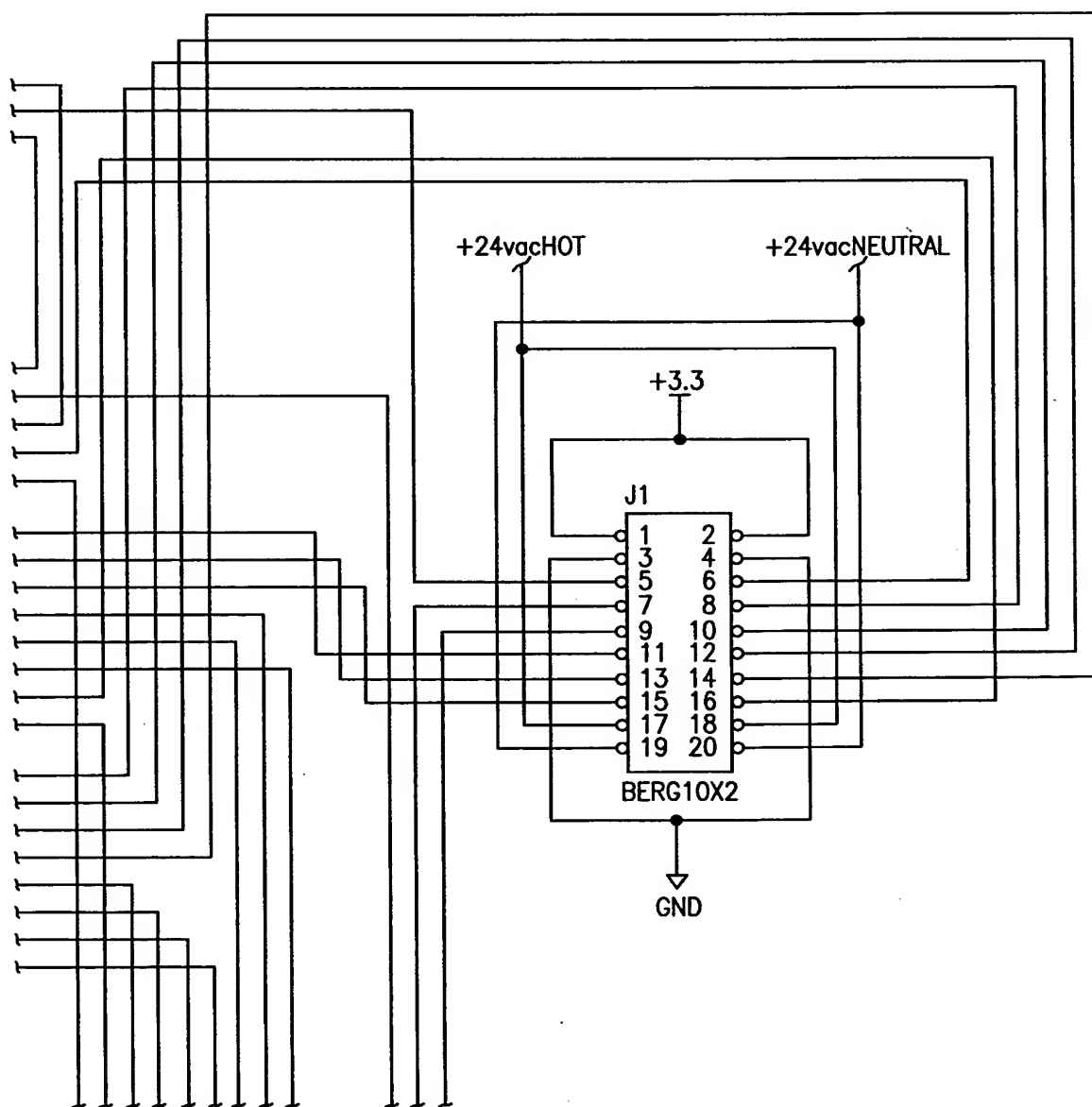


**FIG. 6A-8**

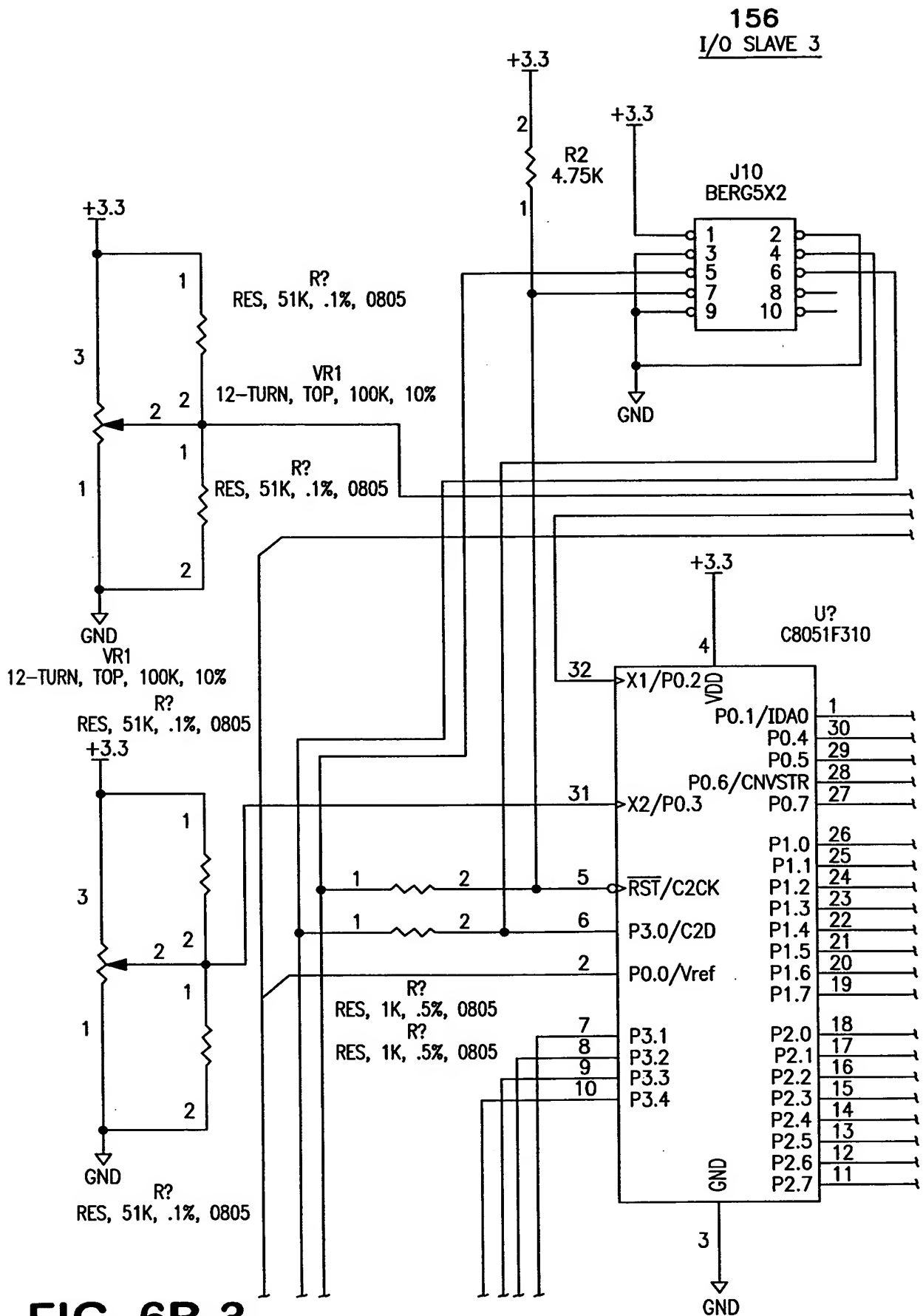


**FIG. 6B-1**

C2 INTERFACE

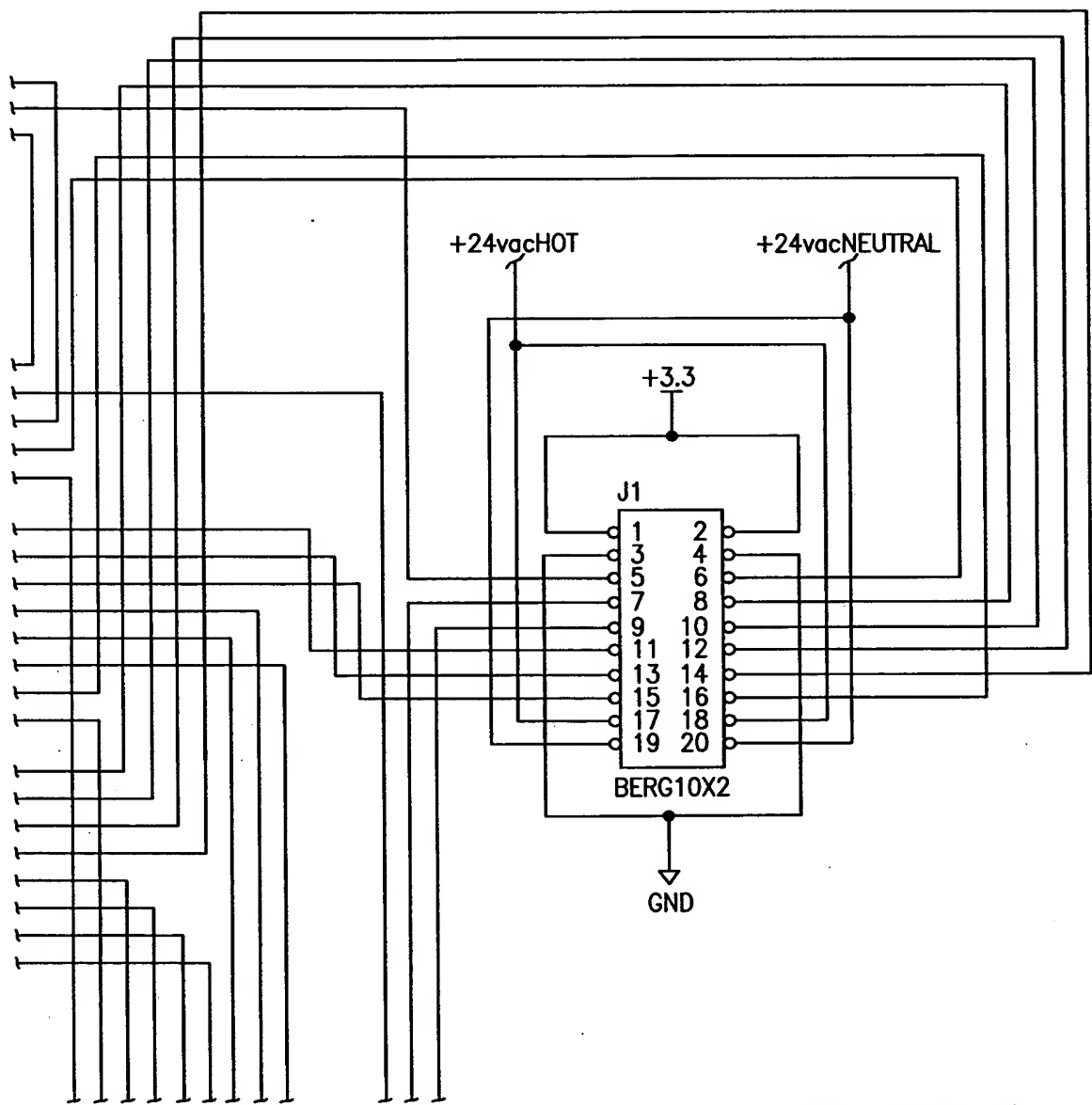


**FIG. 6B-2**

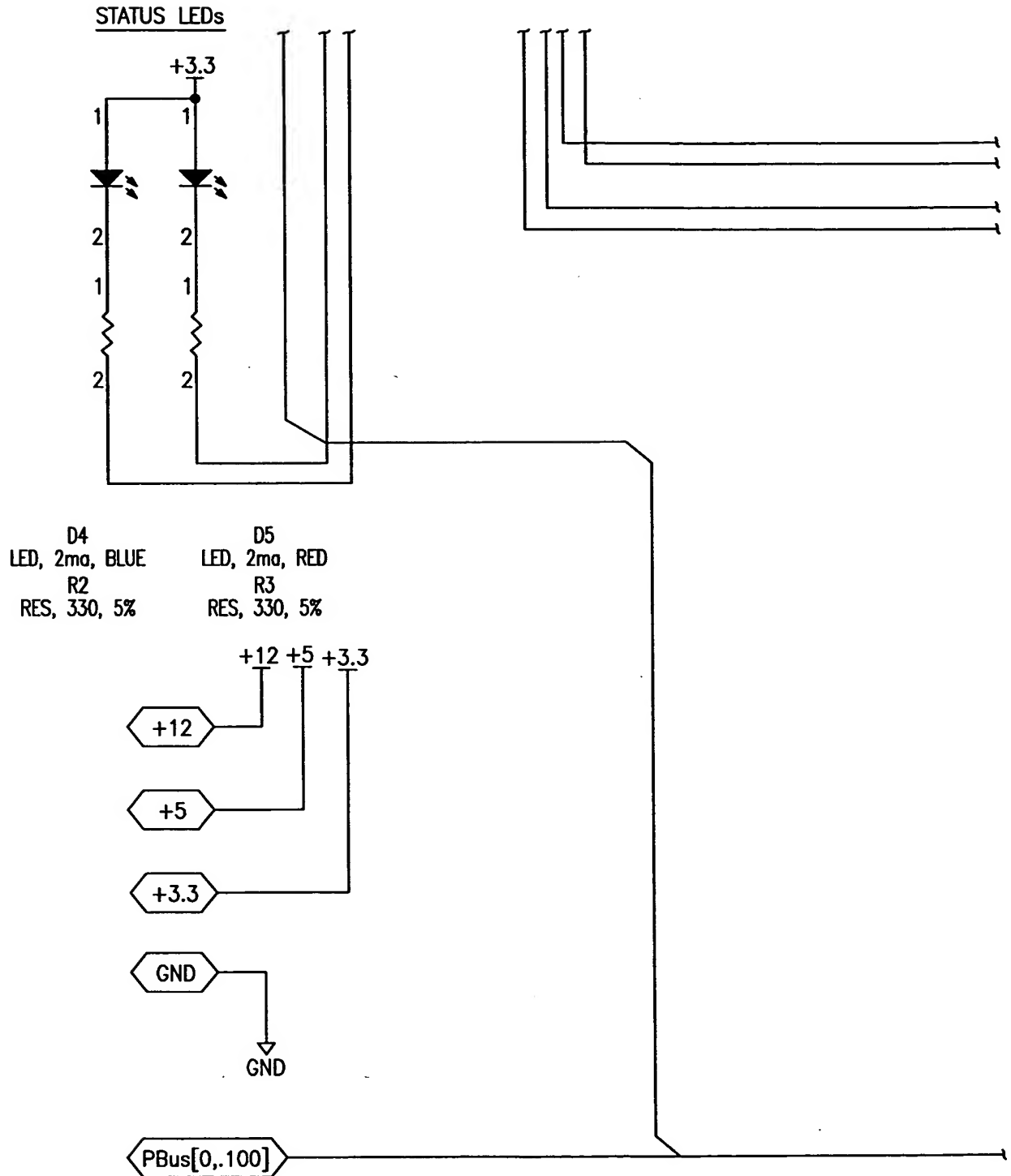


**FIG. 6B-3**

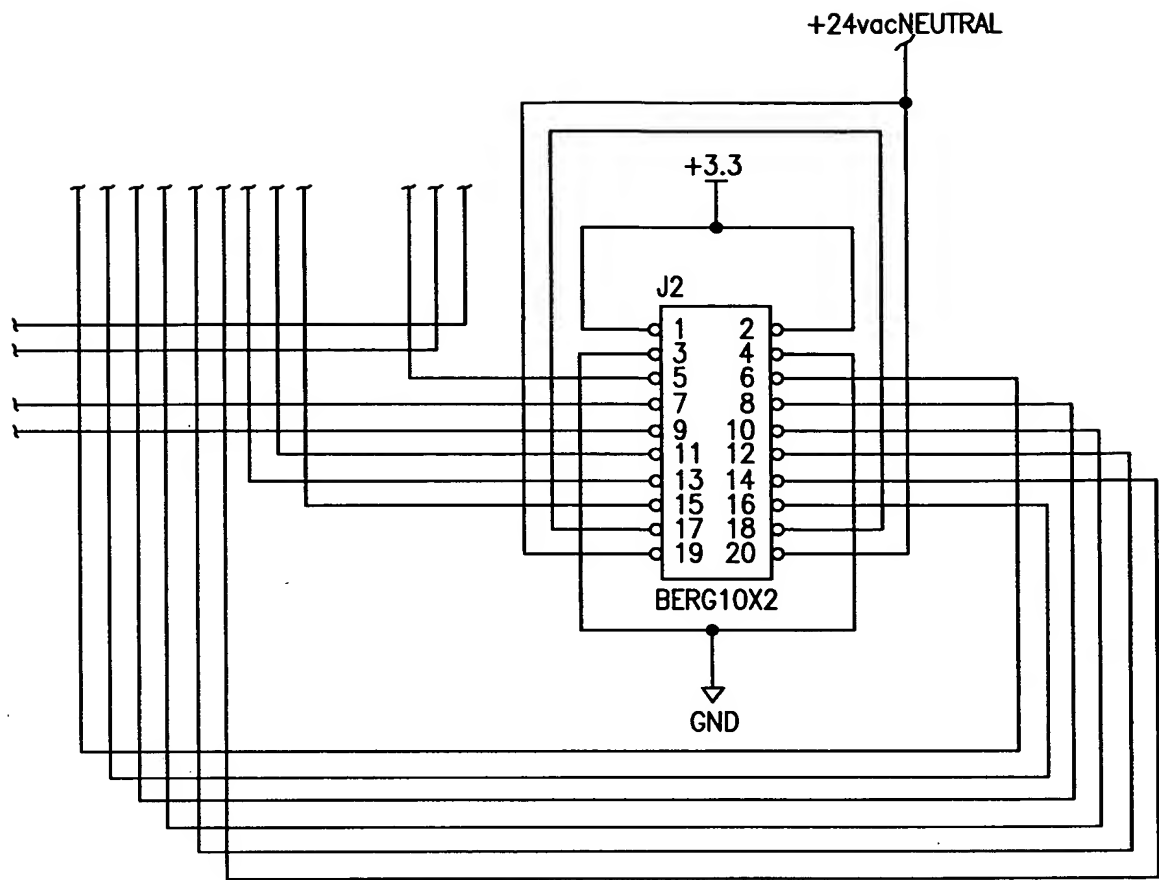
C2 INTERFACE



**FIG. 6B-4**

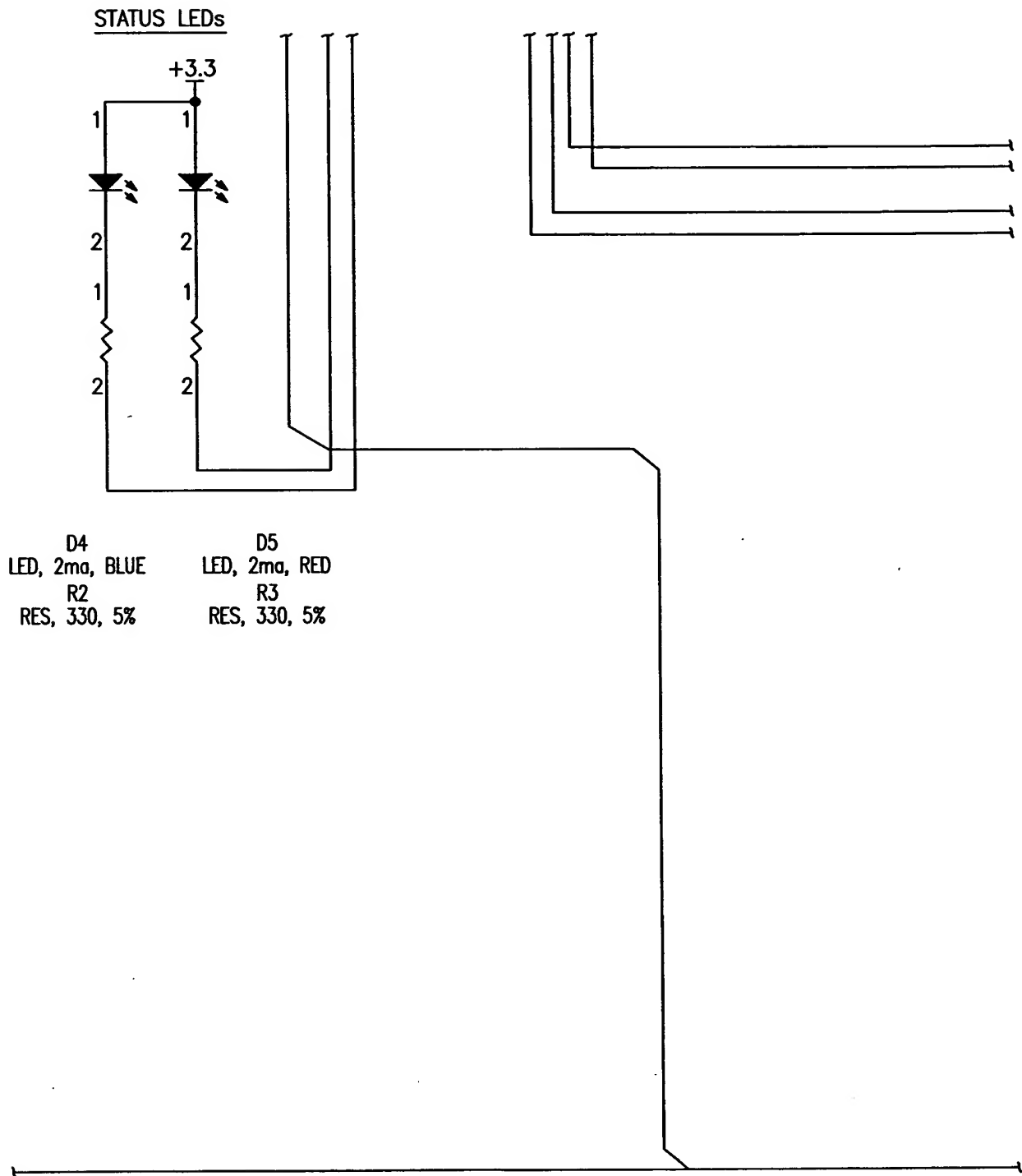


**FIG. 6B-5**

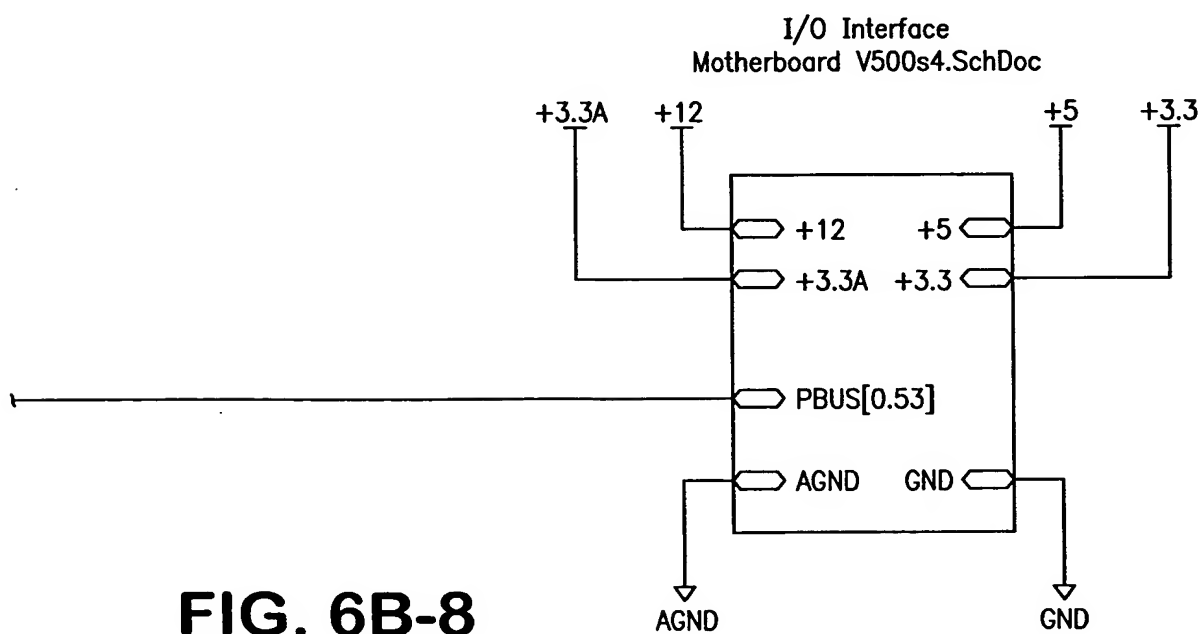
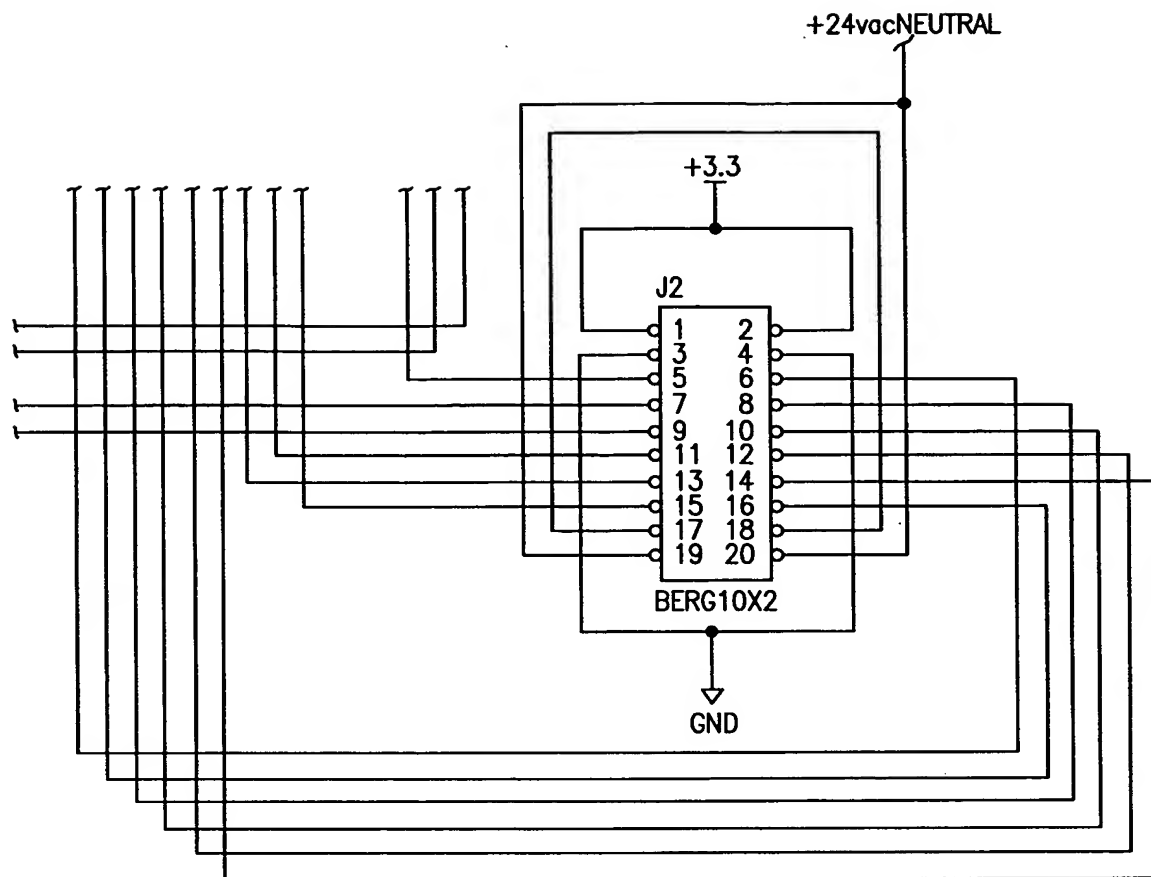


**FIG. 6B-6**





**FIG. 6B-7**

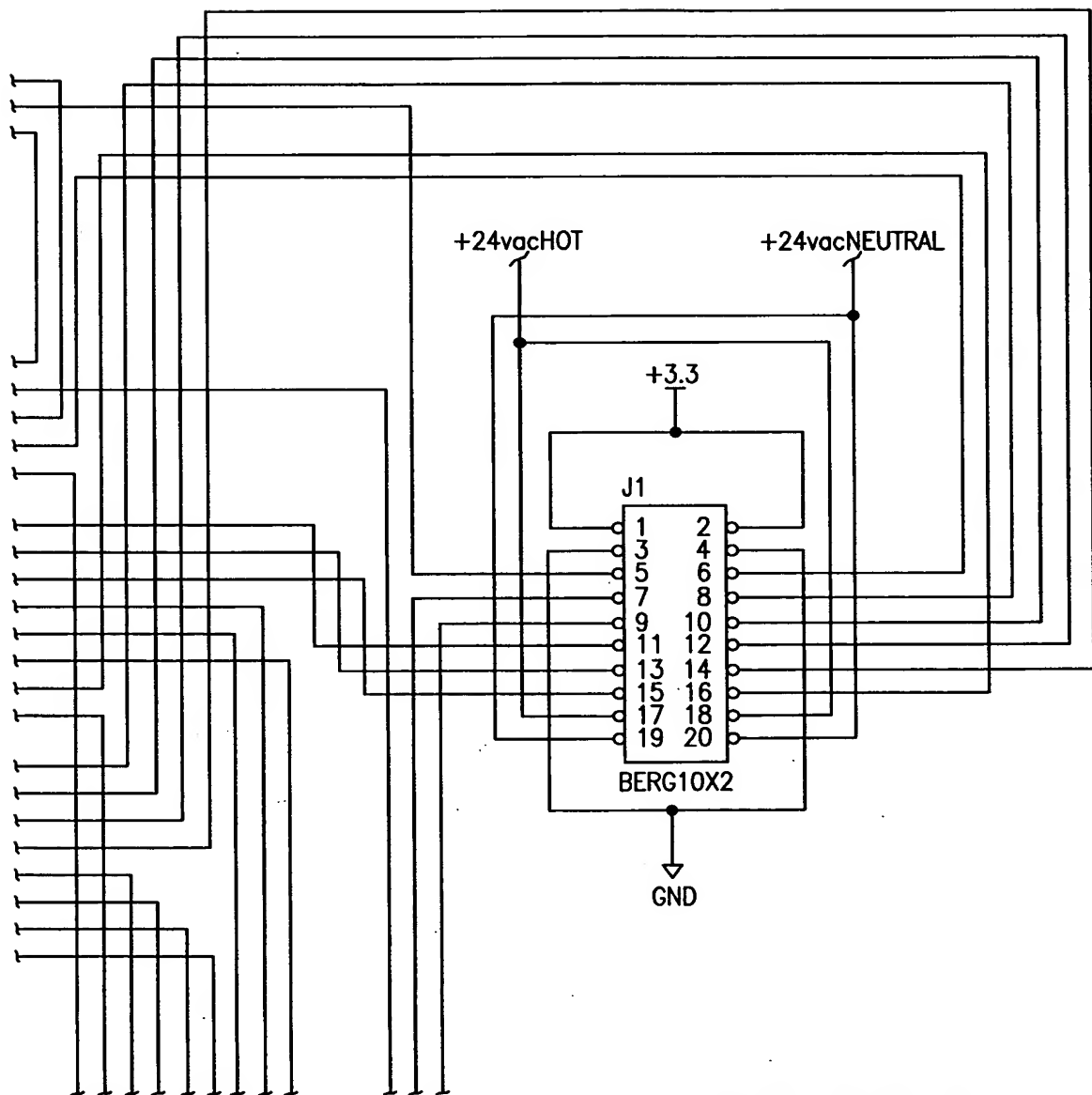


**FIG. 6B-8**



**FIG. 6C-1**

C2 INTERFACE



**FIG. 6C-2**

158  
I/O SLAVE 6

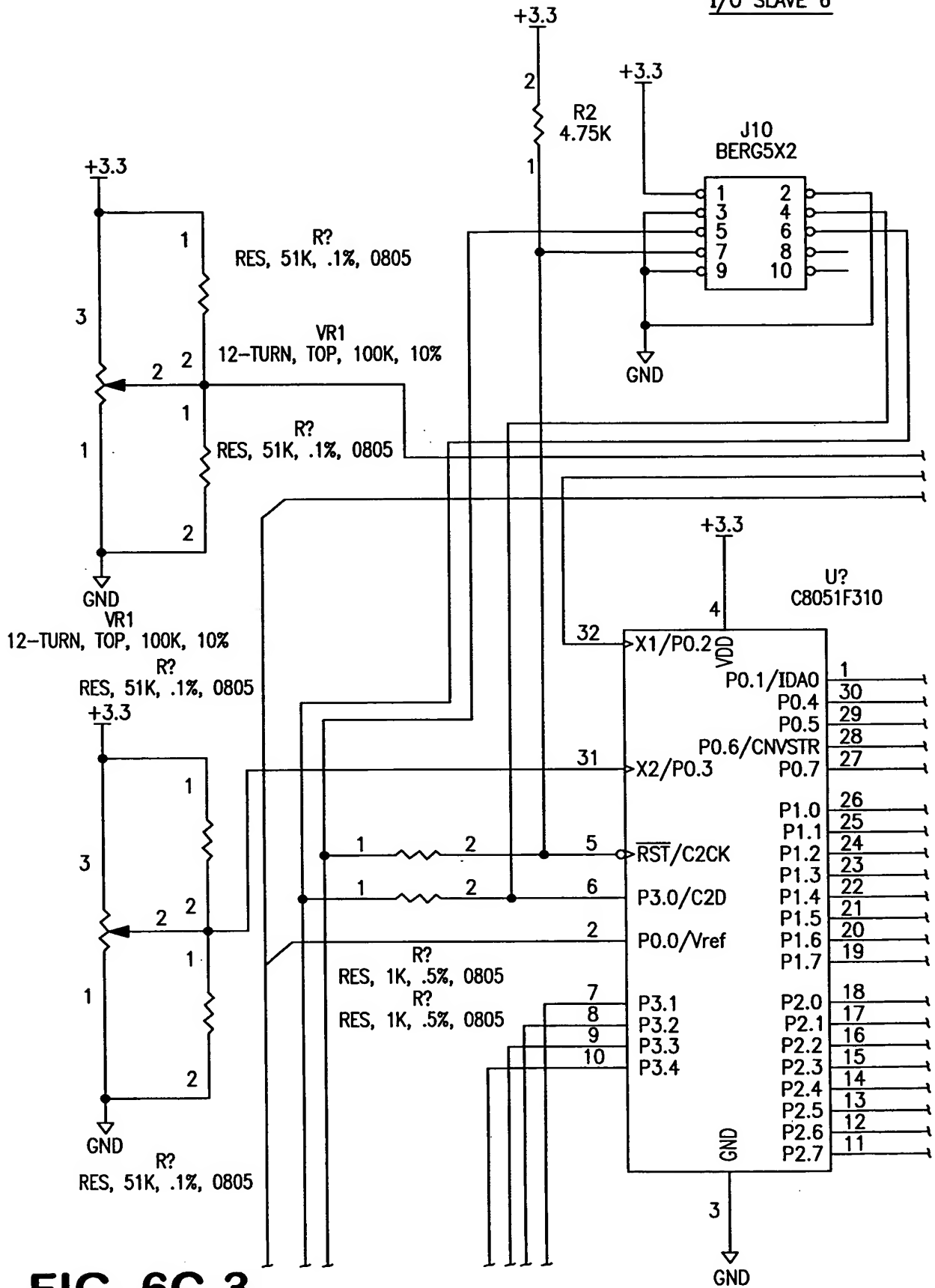
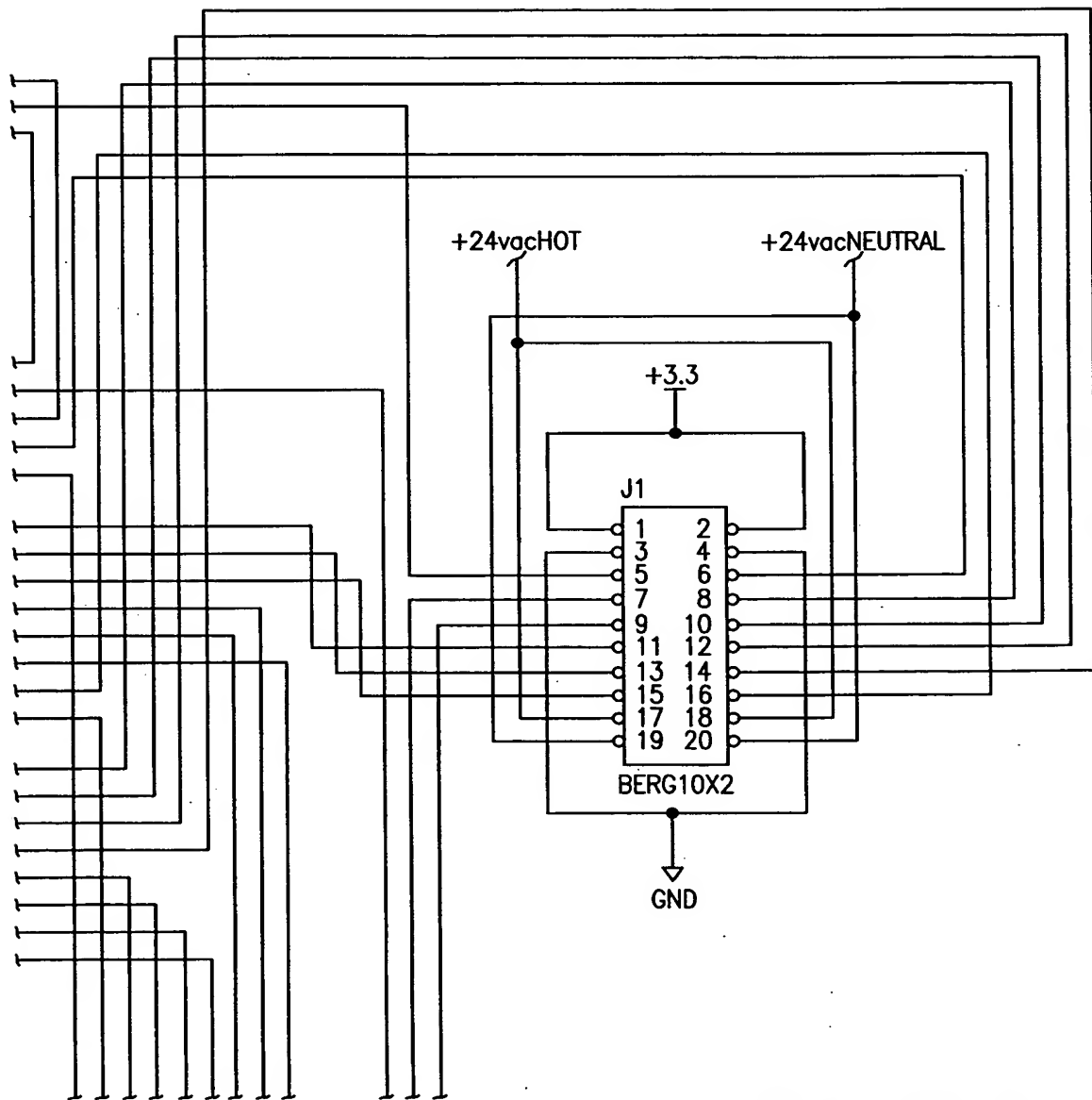
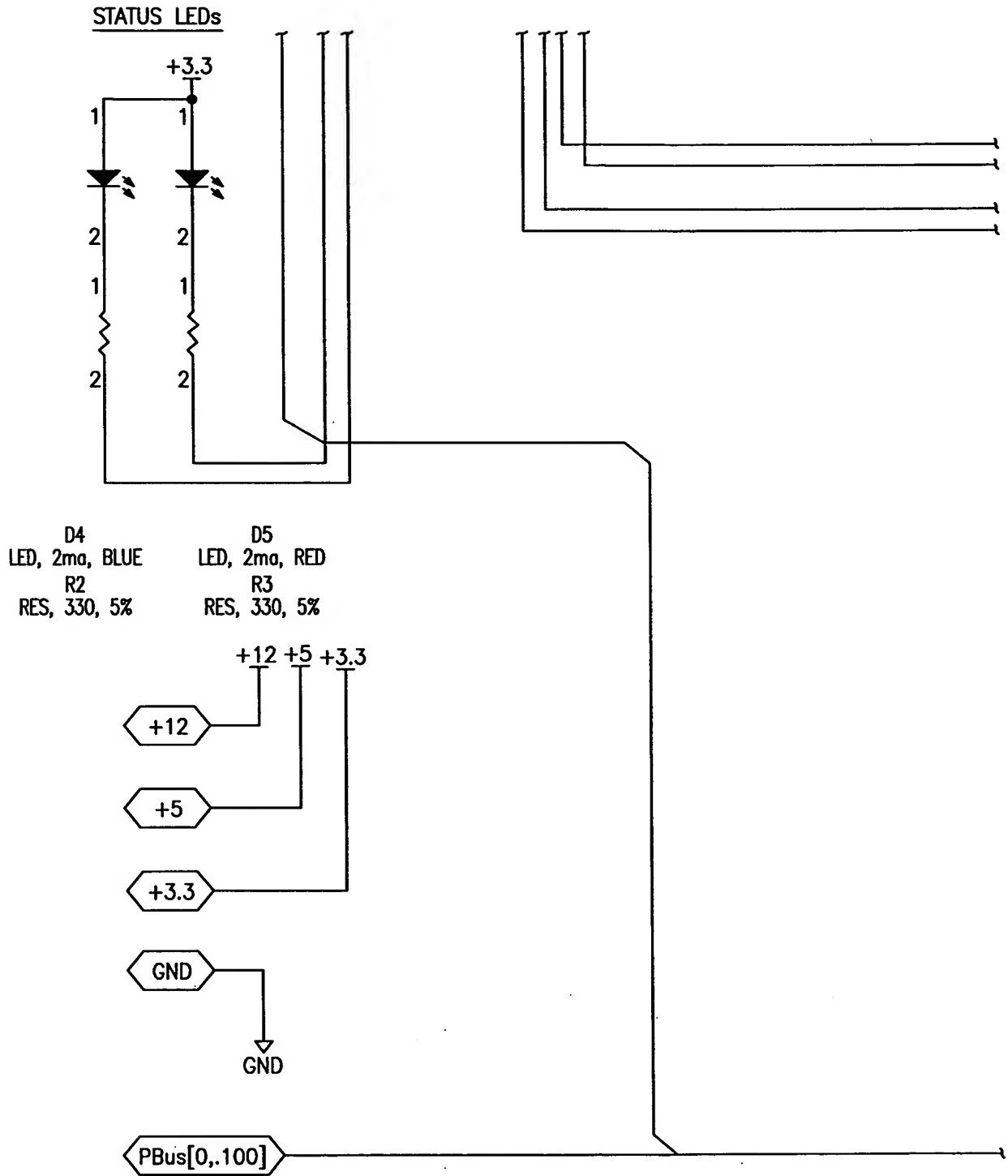


FIG. 6C-3

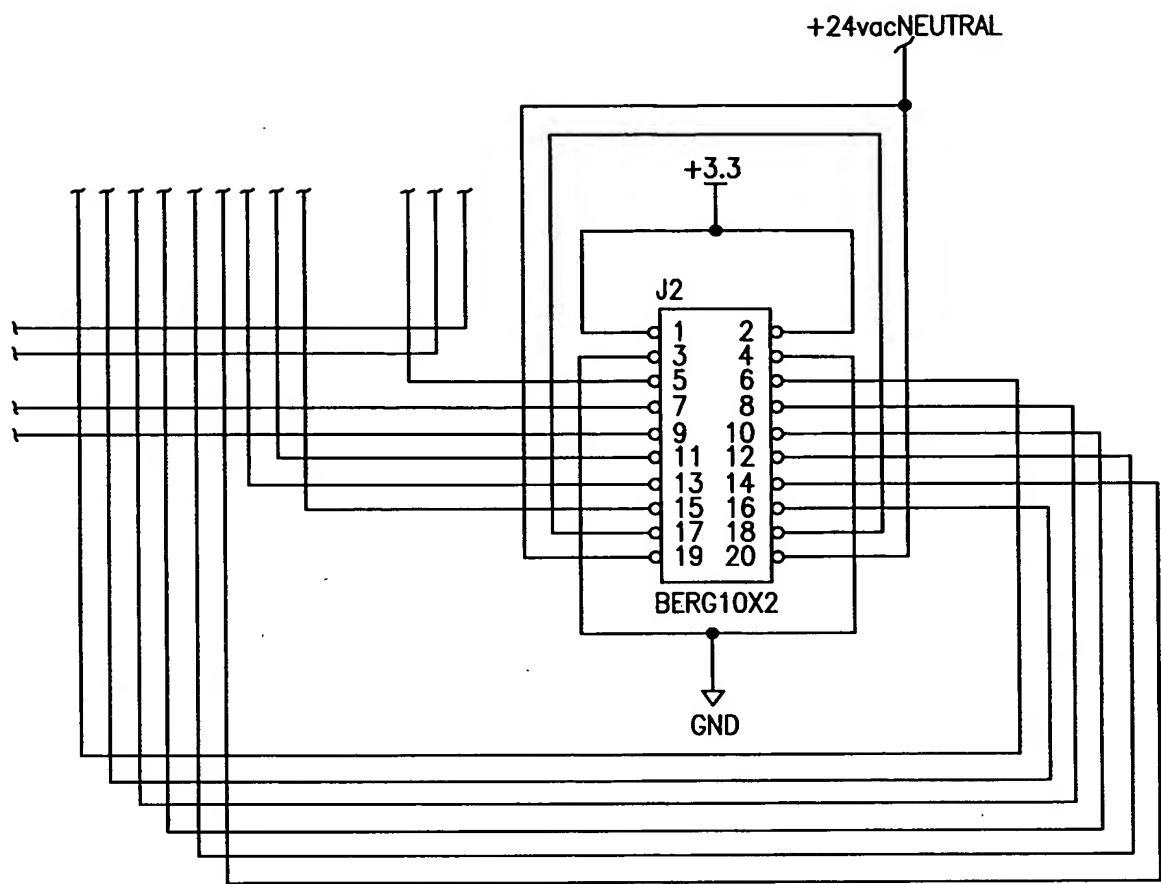
C2 INTERFACE



**FIG. 6C-4**

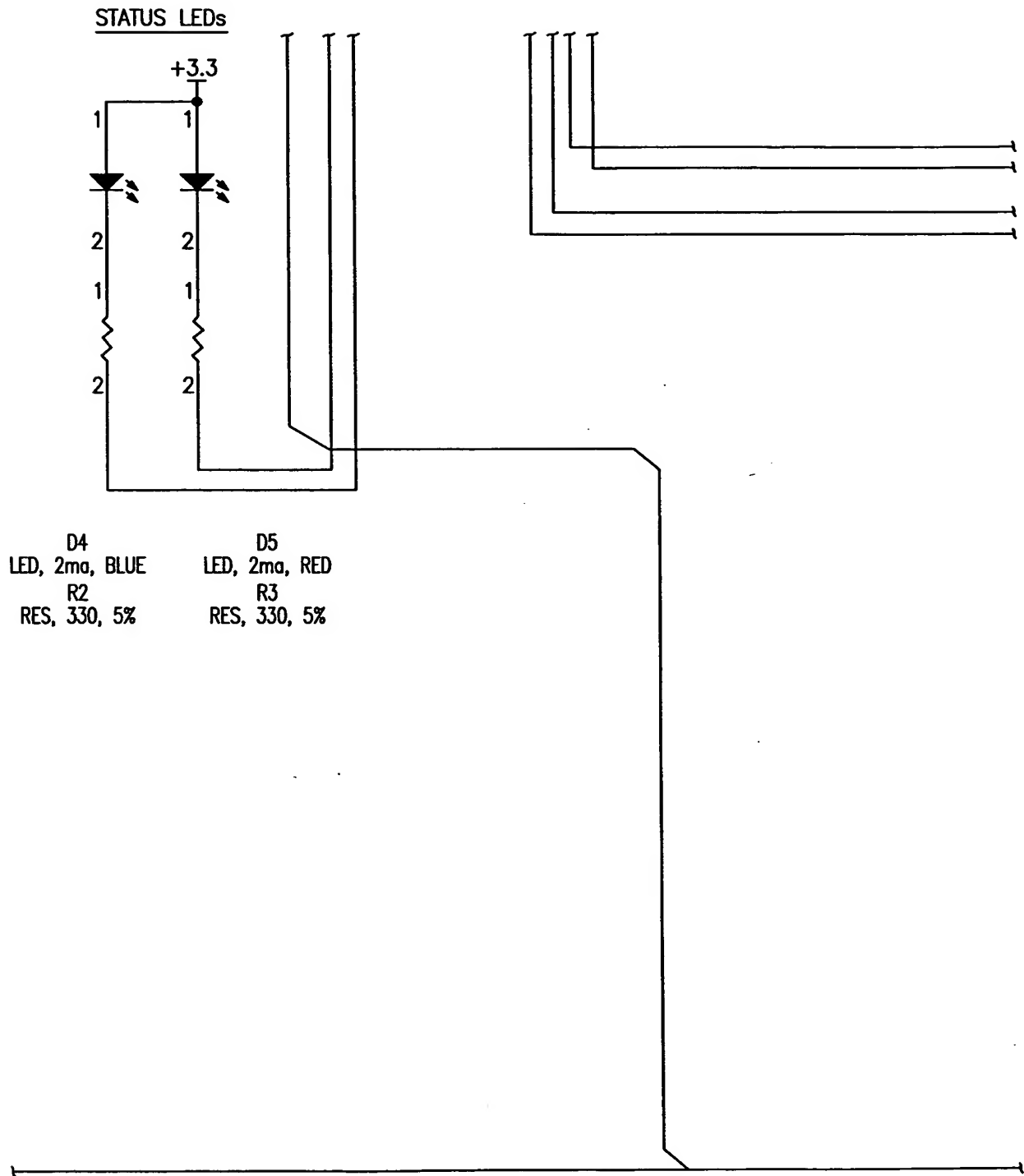


**FIG. 6C-5**

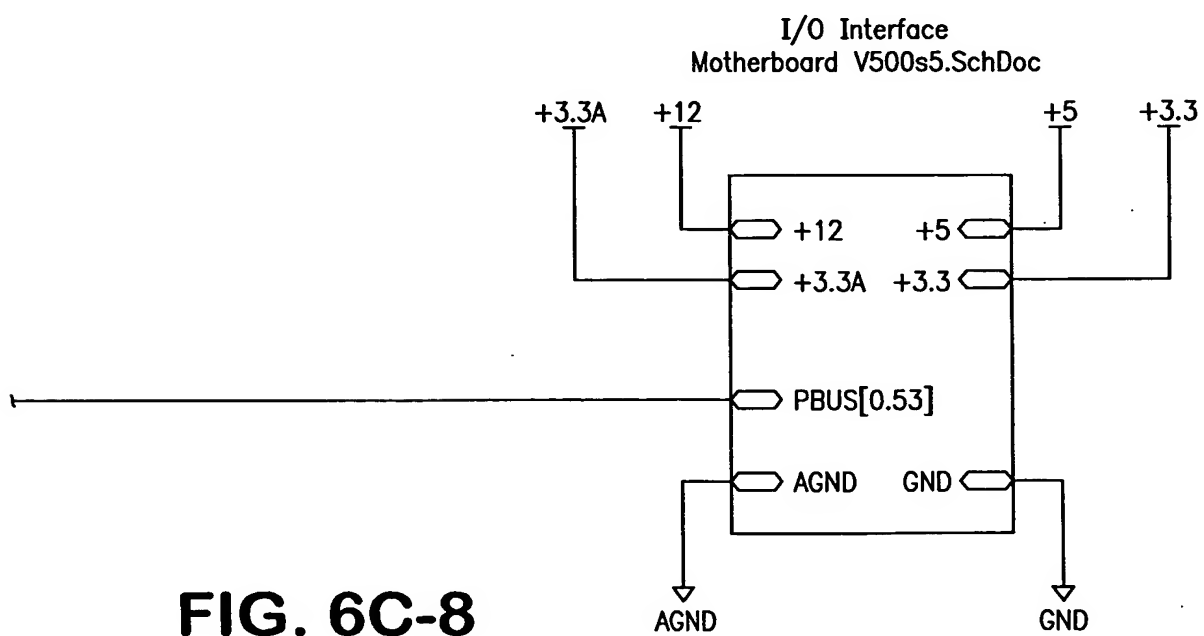
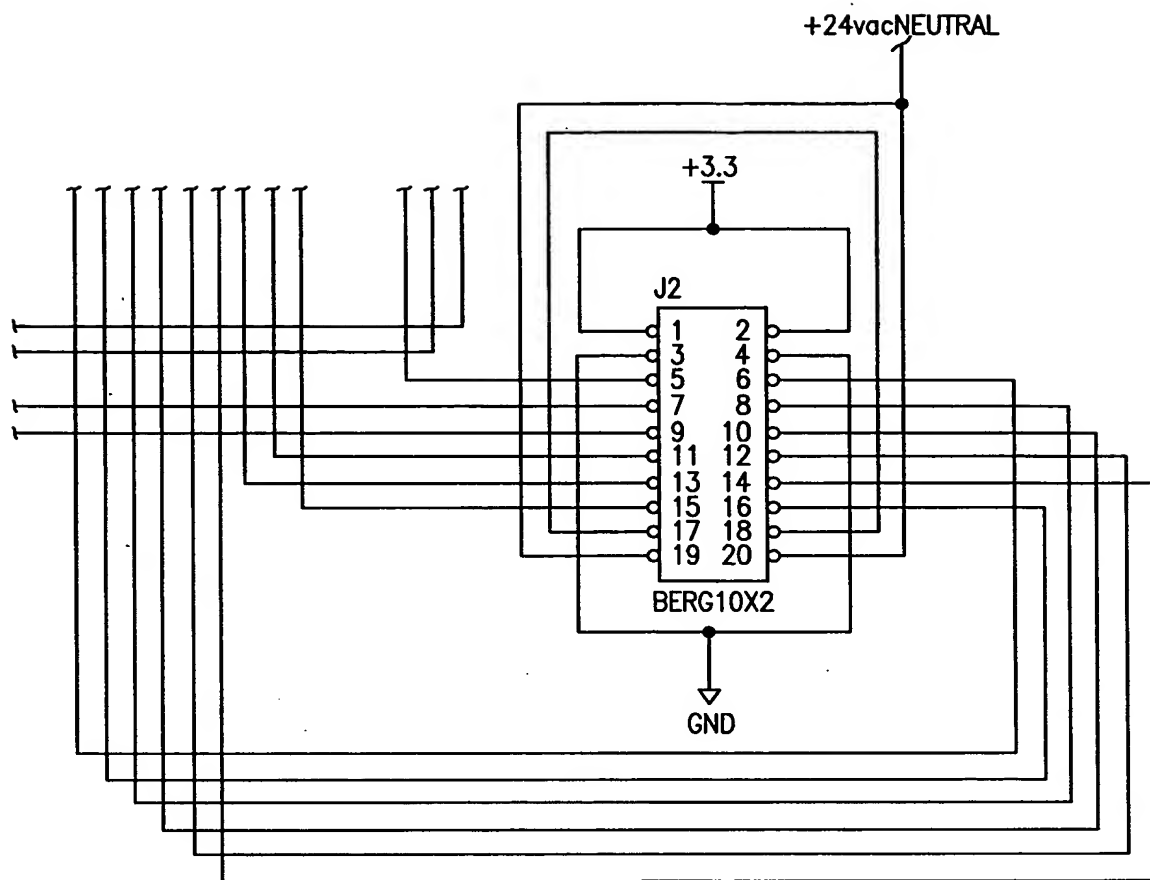


**FIG. 6C-6**





**FIG. 6C-7**



**FIG. 6C-8**

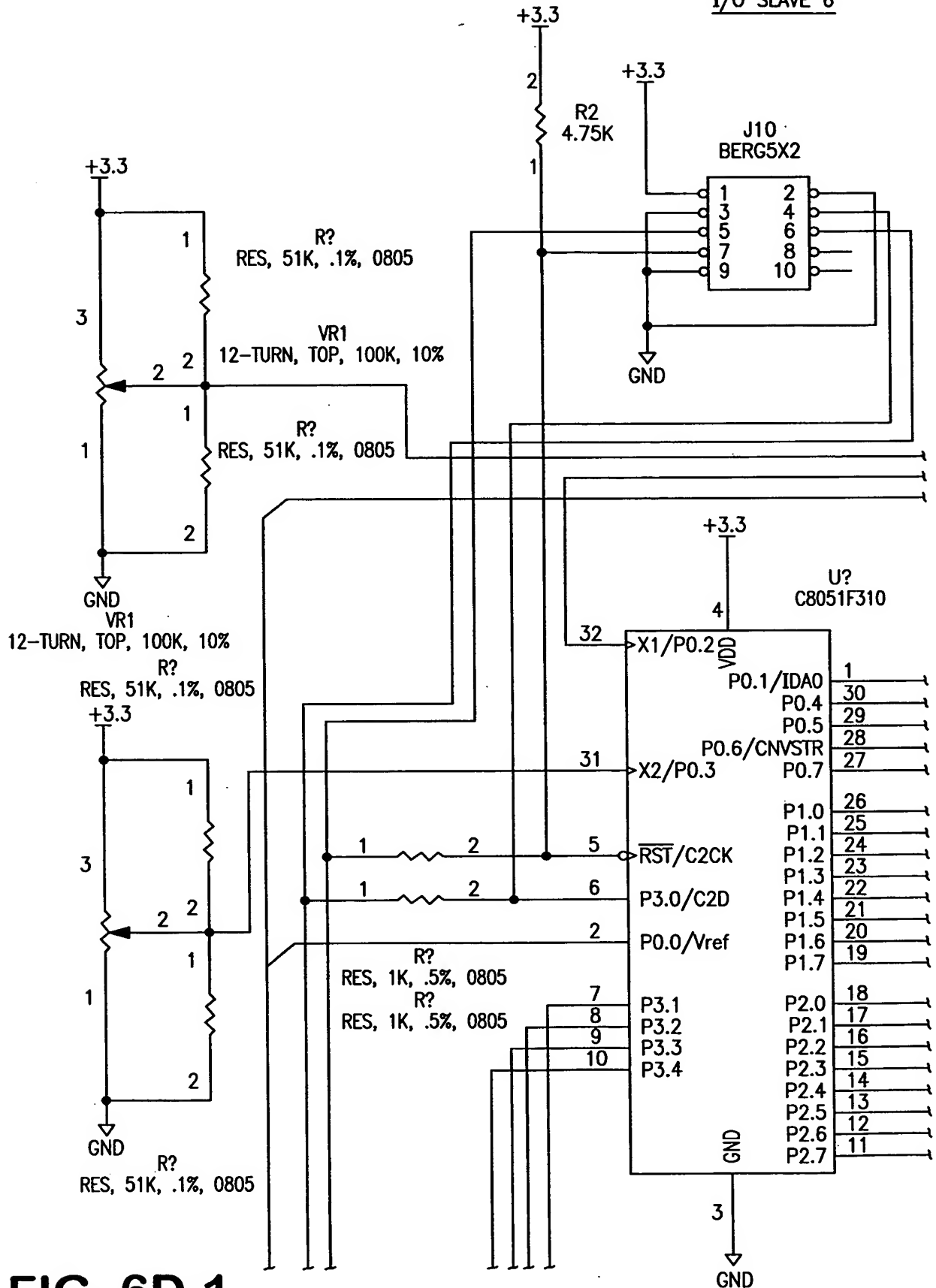
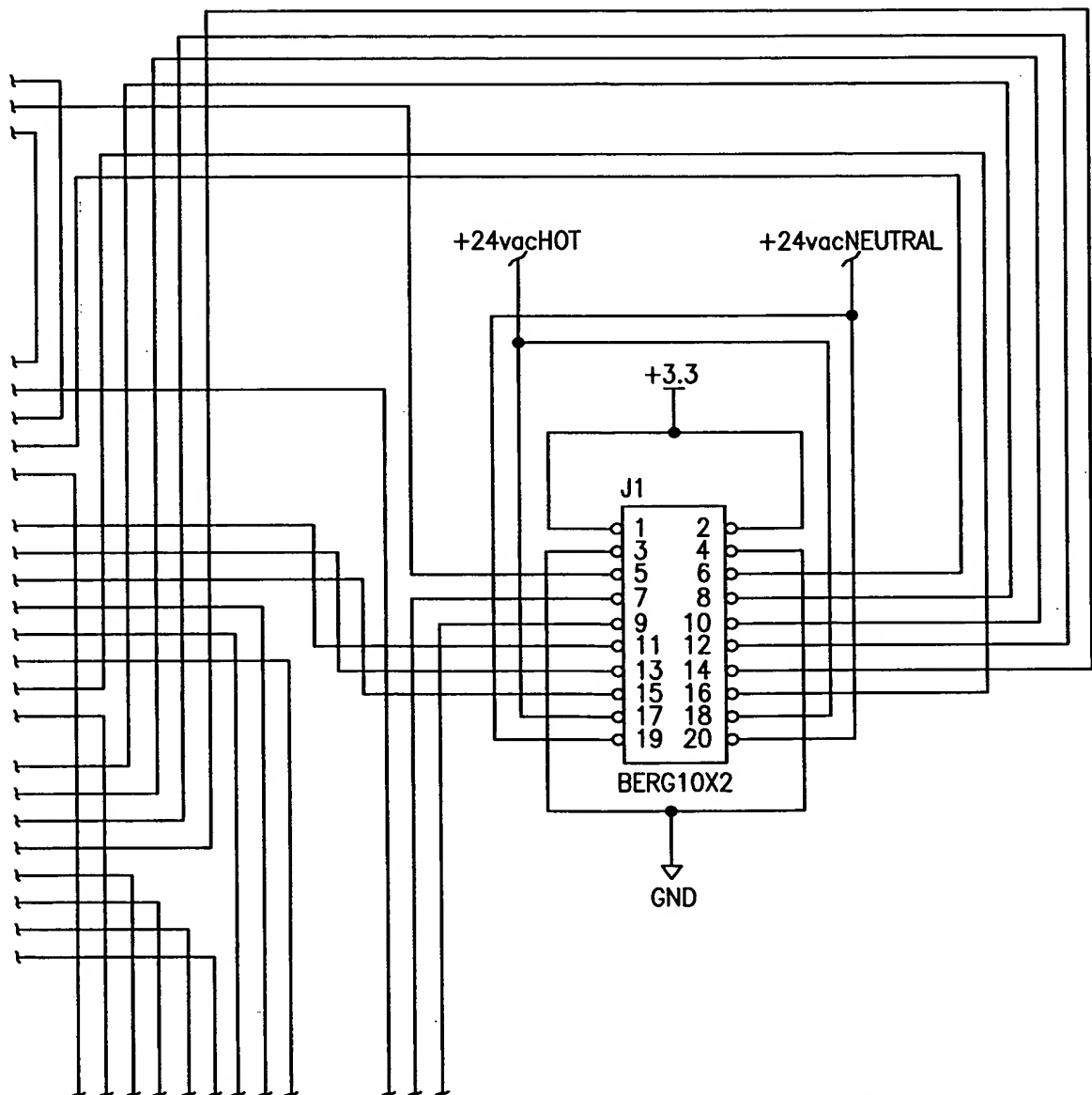
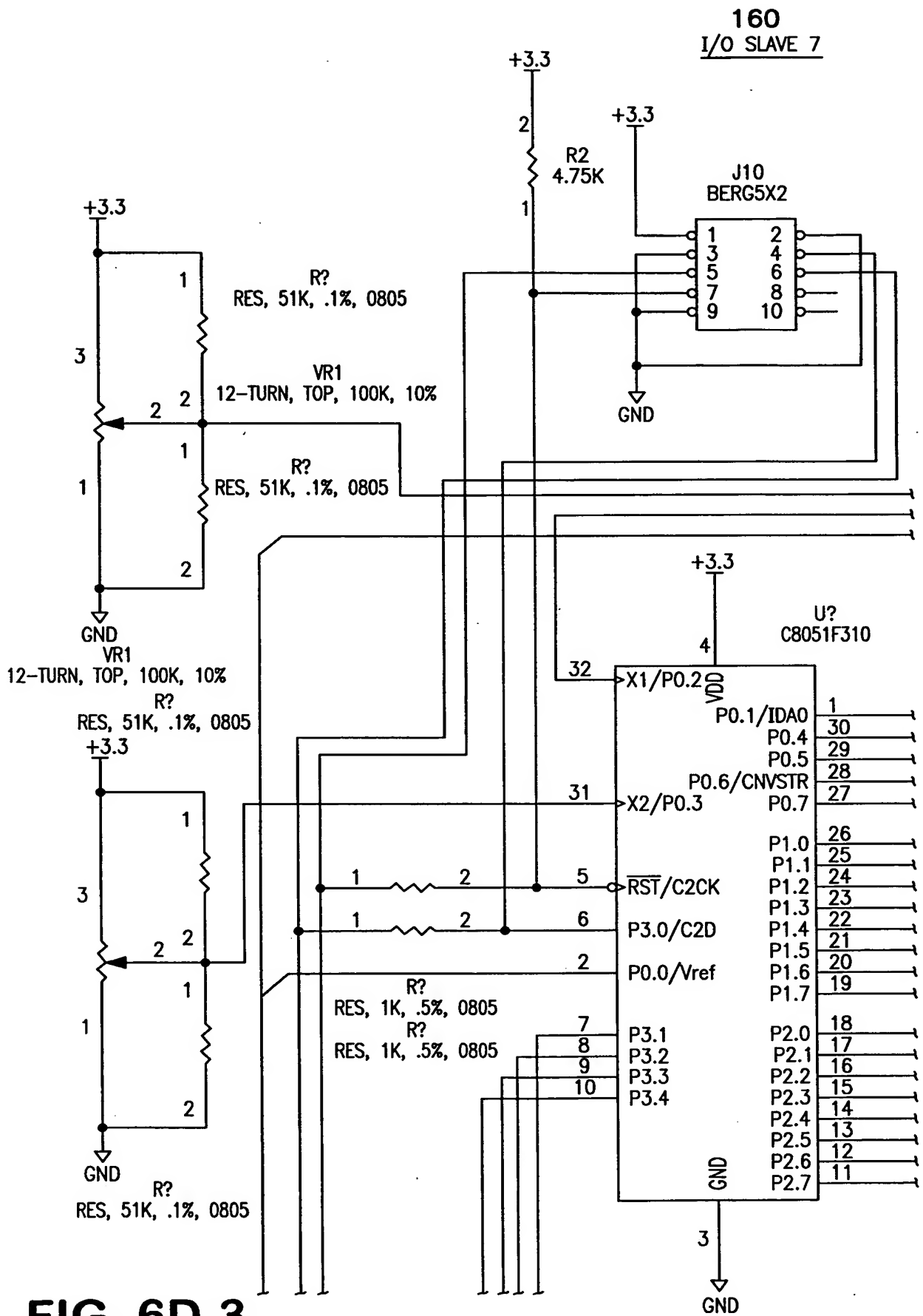


FIG. 6D-1

C2 INTERFACE

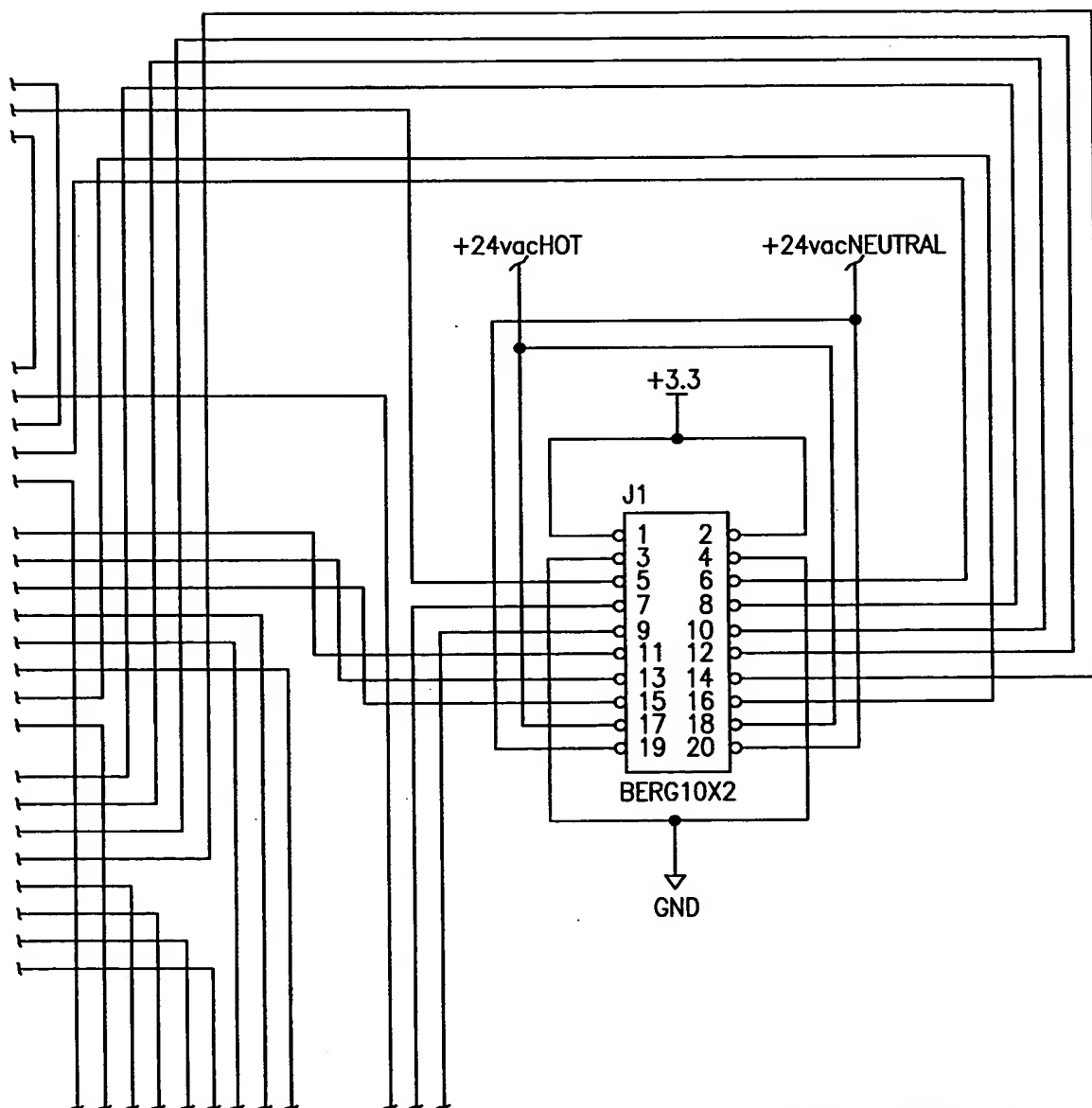


**FIG. 6D-2**

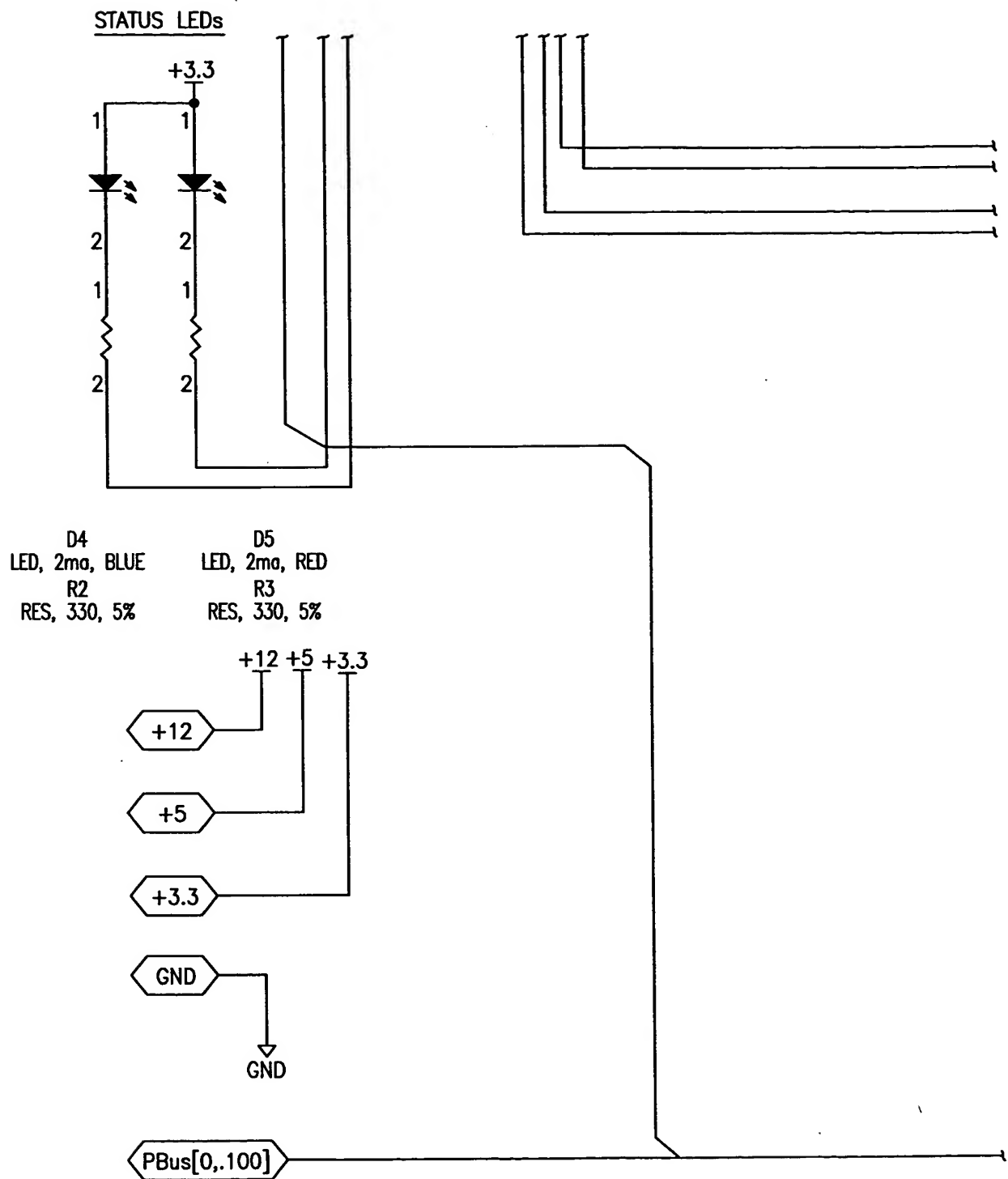


**FIG. 6D-3**

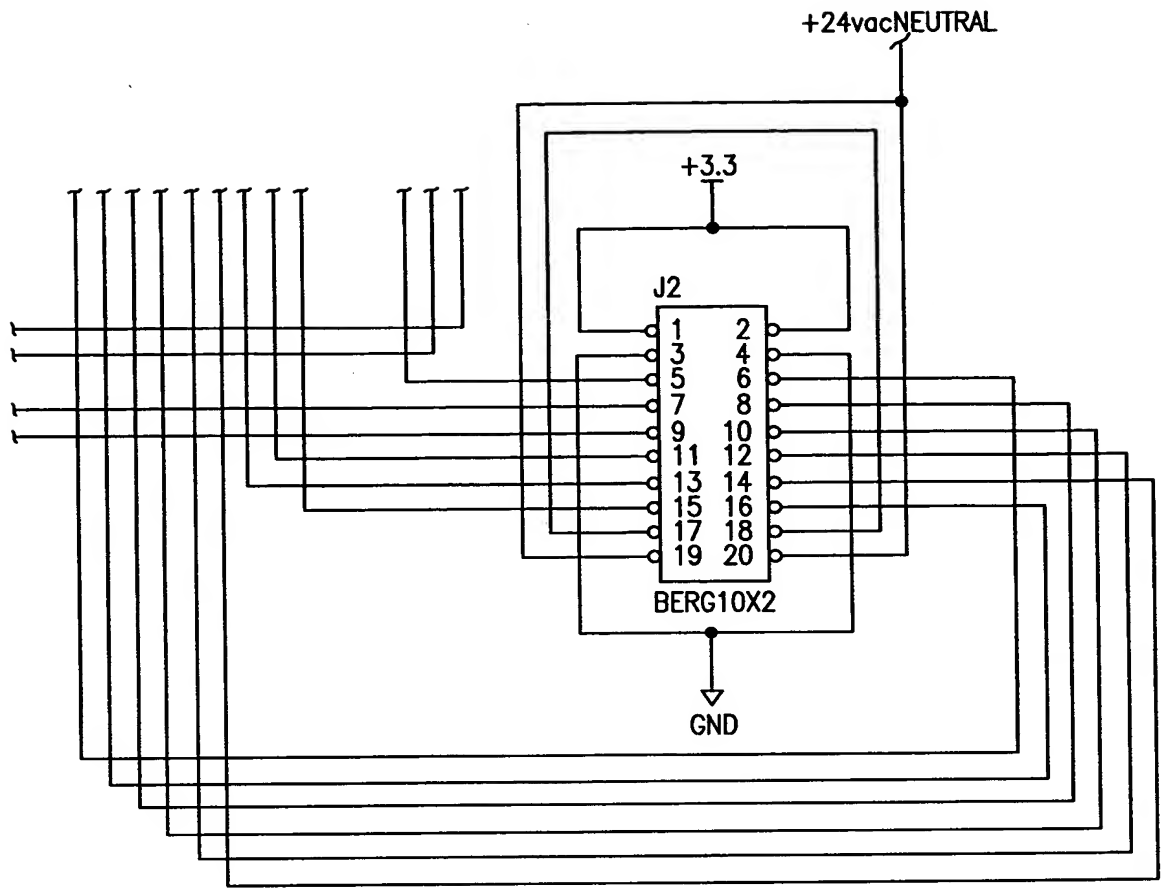
C2 INTERFACE



**FIG. 6D-4**



**FIG. 6D-5**



**FIG. 6D-6**



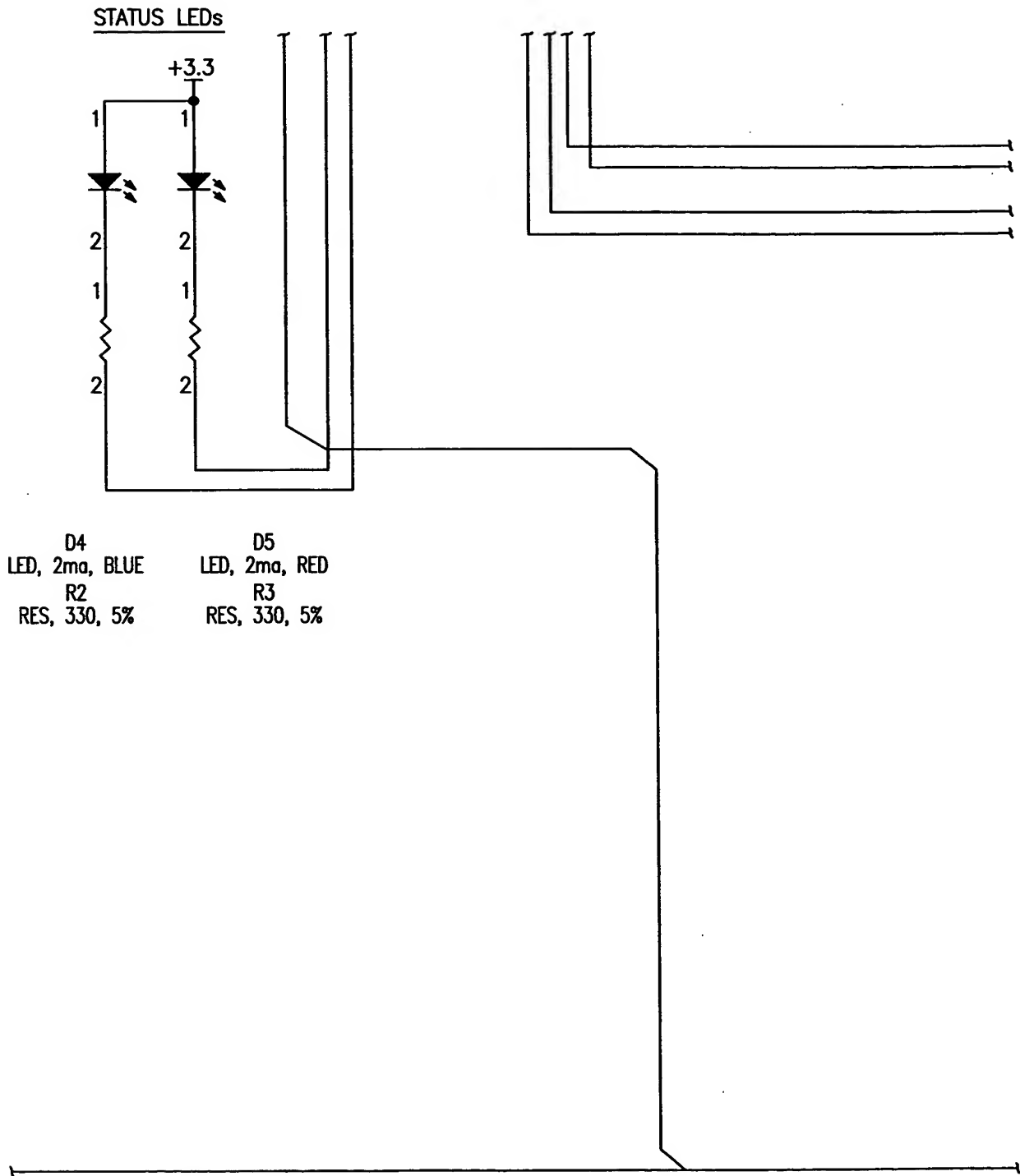
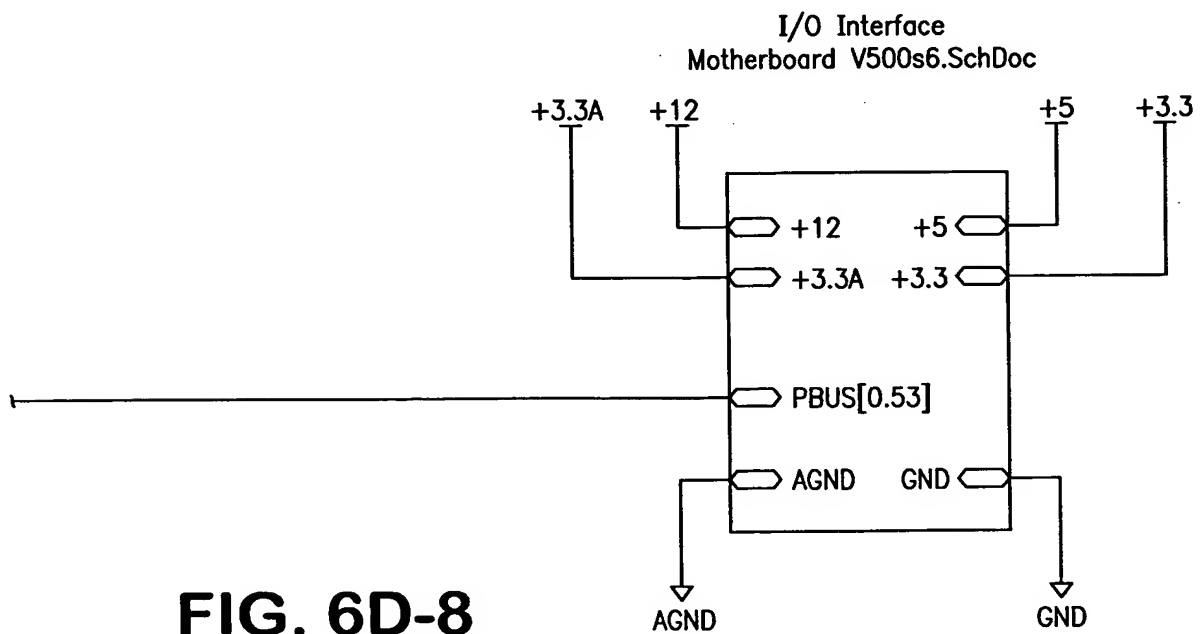
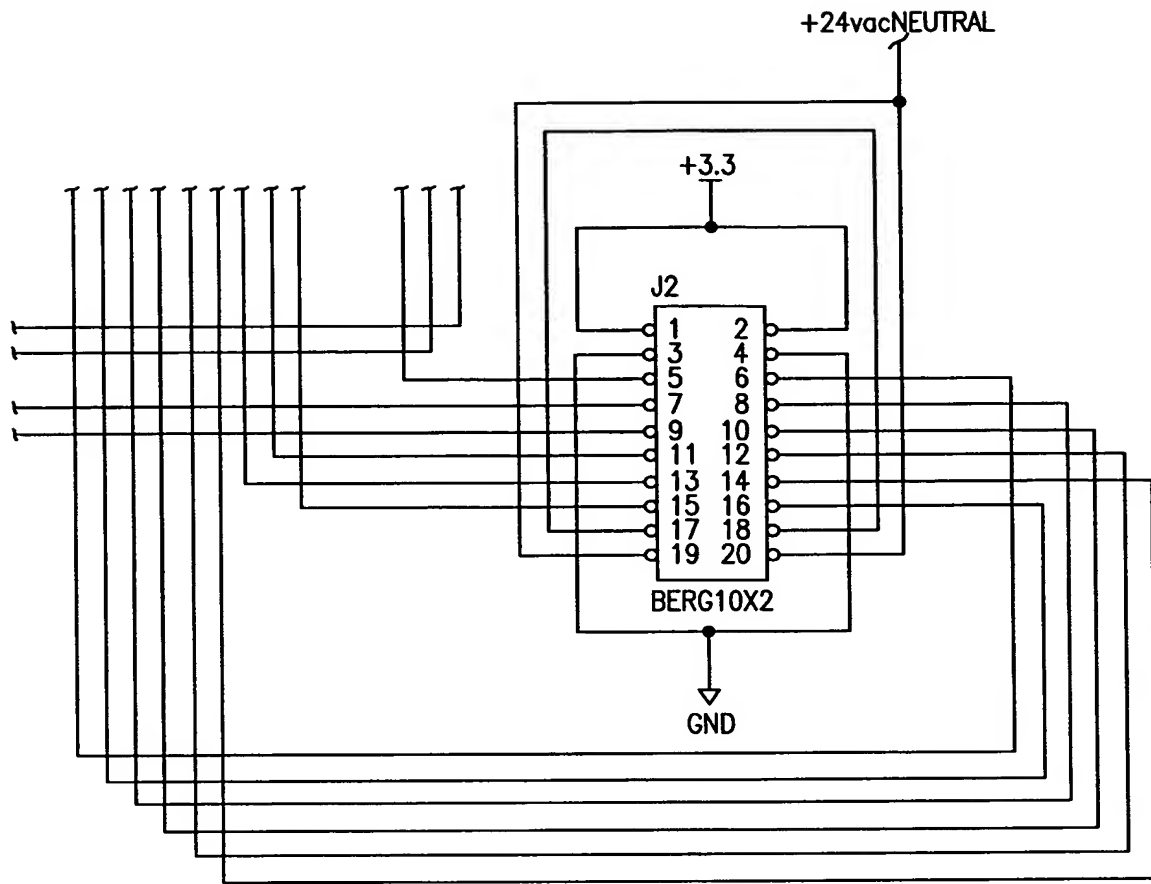


FIG. 6D-7

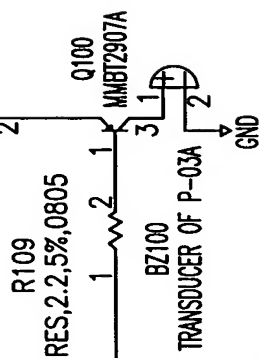


**FIG. 6D-8**

161

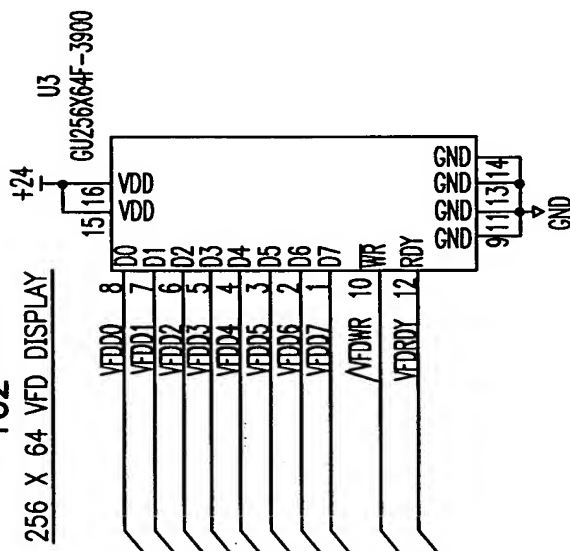
ALARM BUZZER

+3.3 DISPLAY



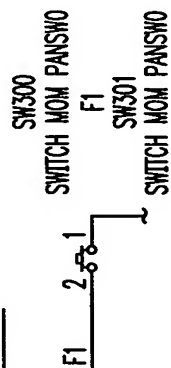
162

256 X 64 VFD DISPLAY



163

USER KEYPAD



164

WARNING LED

+3.3 DISPLAY

STATUS LED

+3.3 DISPLAY

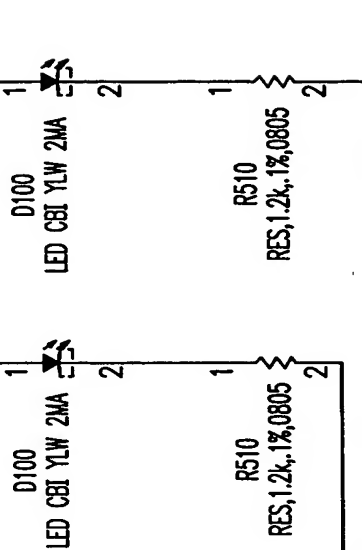
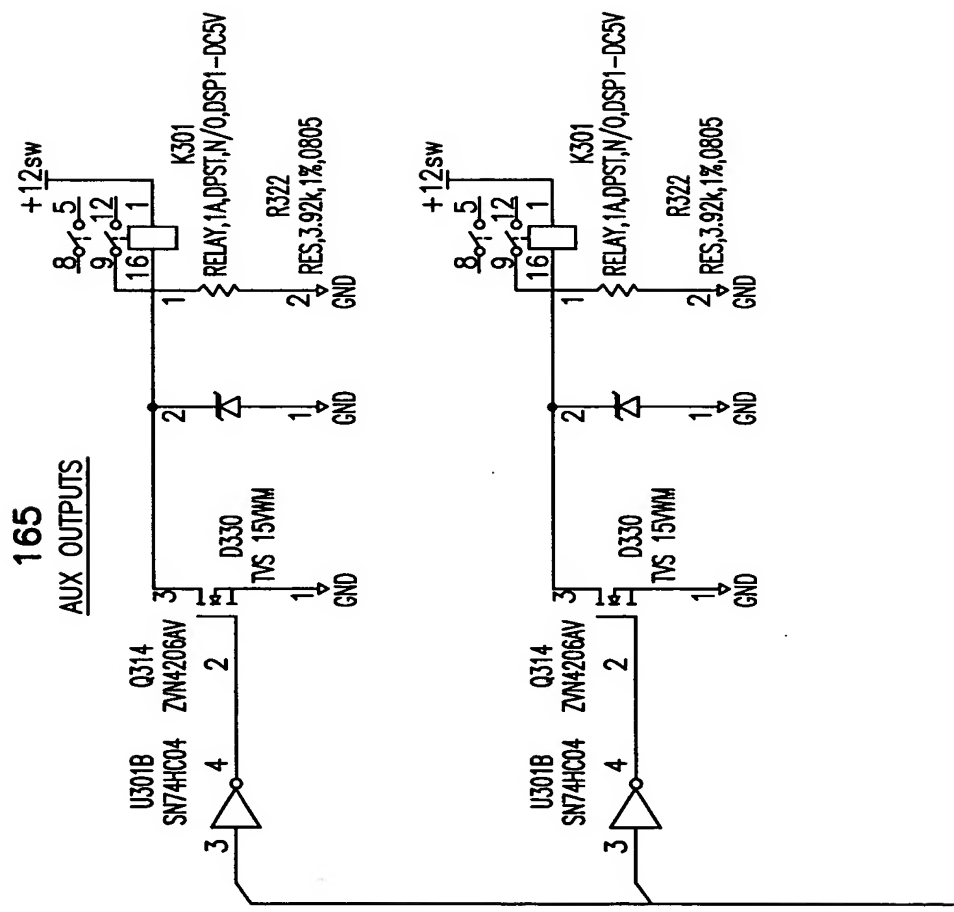
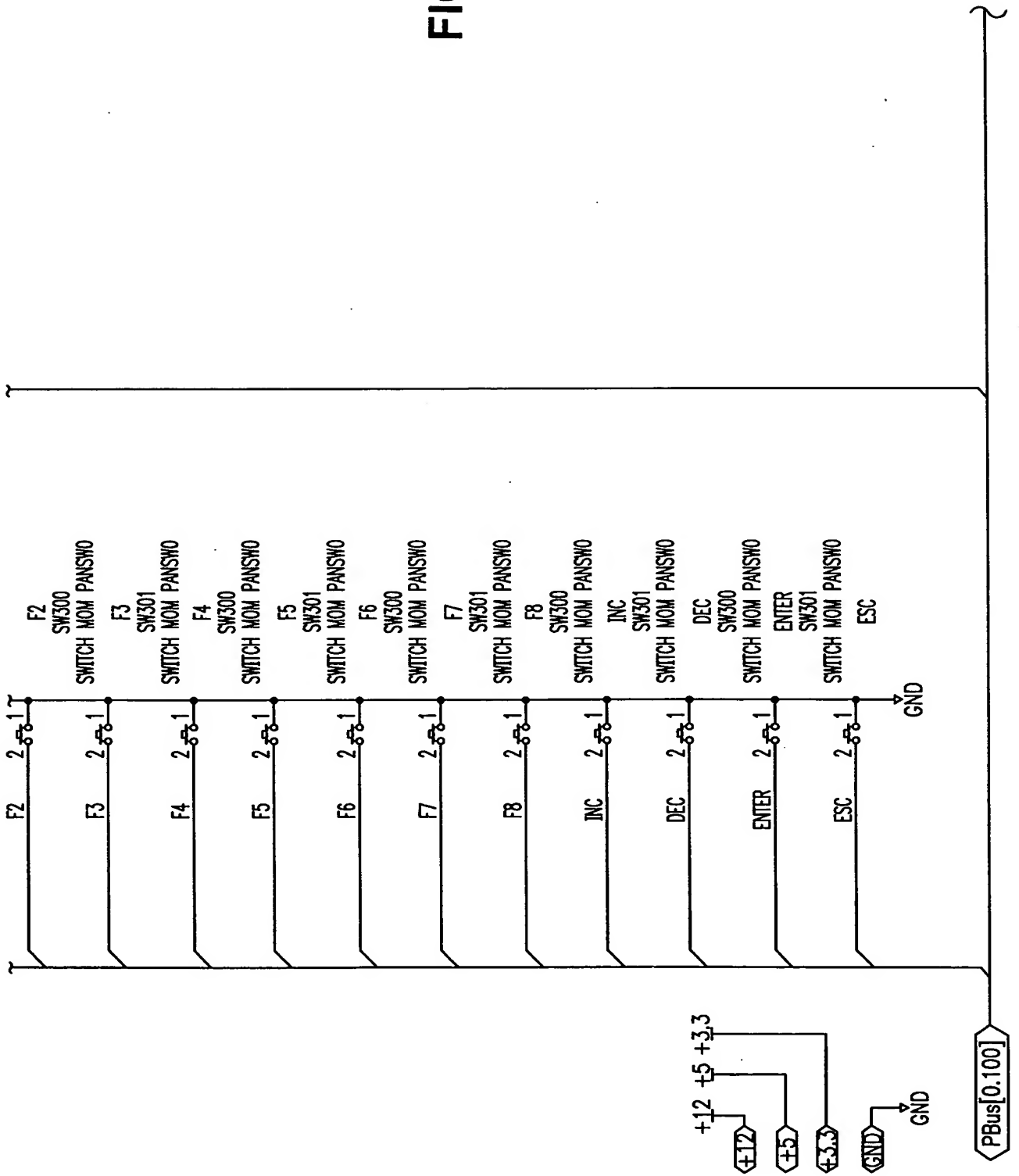
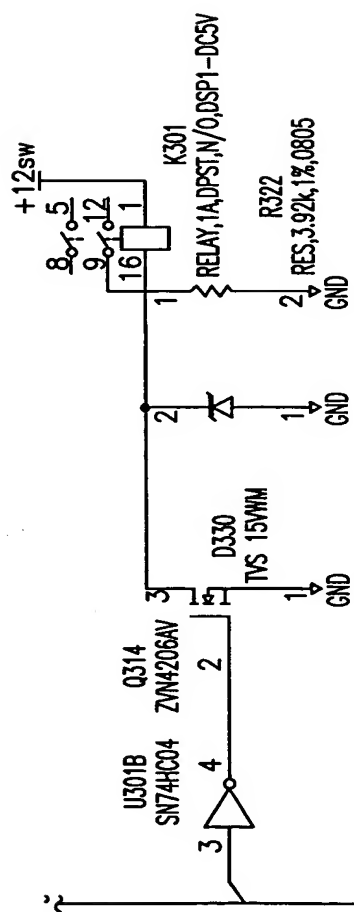


FIG. 7-1

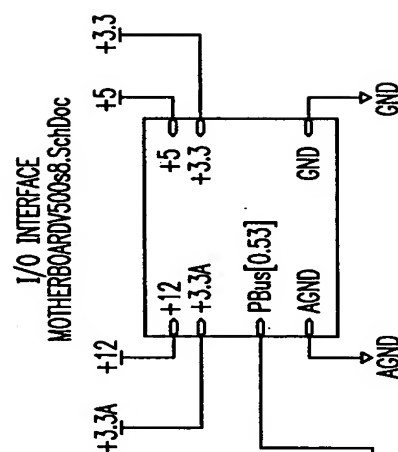
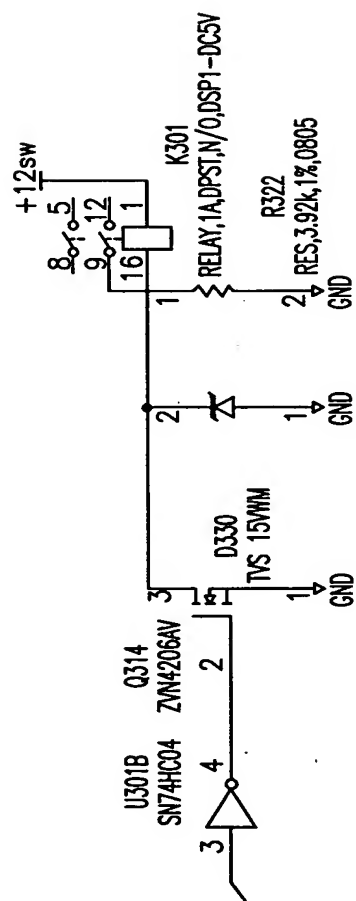


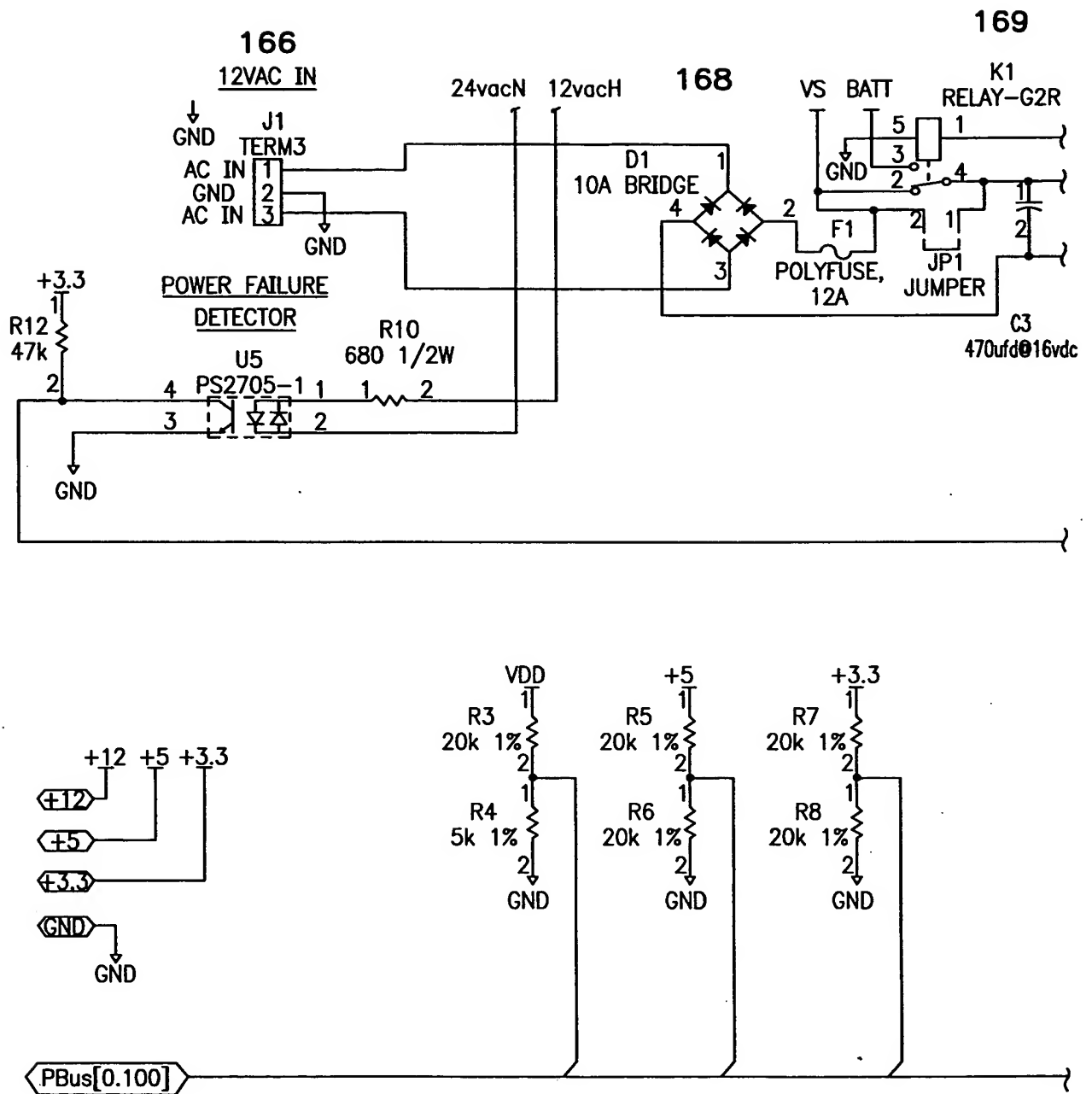
**FIG. 7-2**



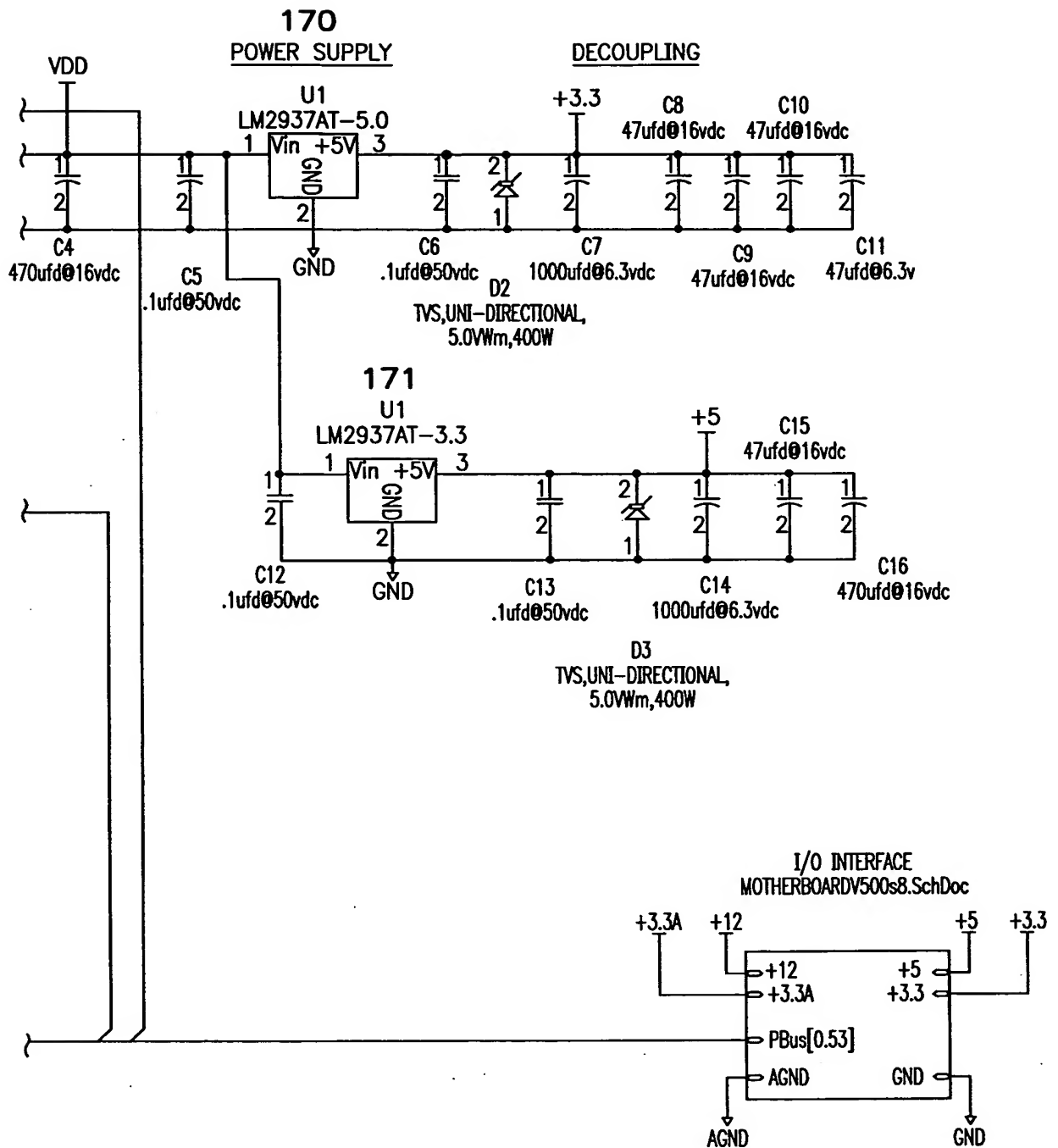


**FIG. 7-4**





**FIG. 8-1**

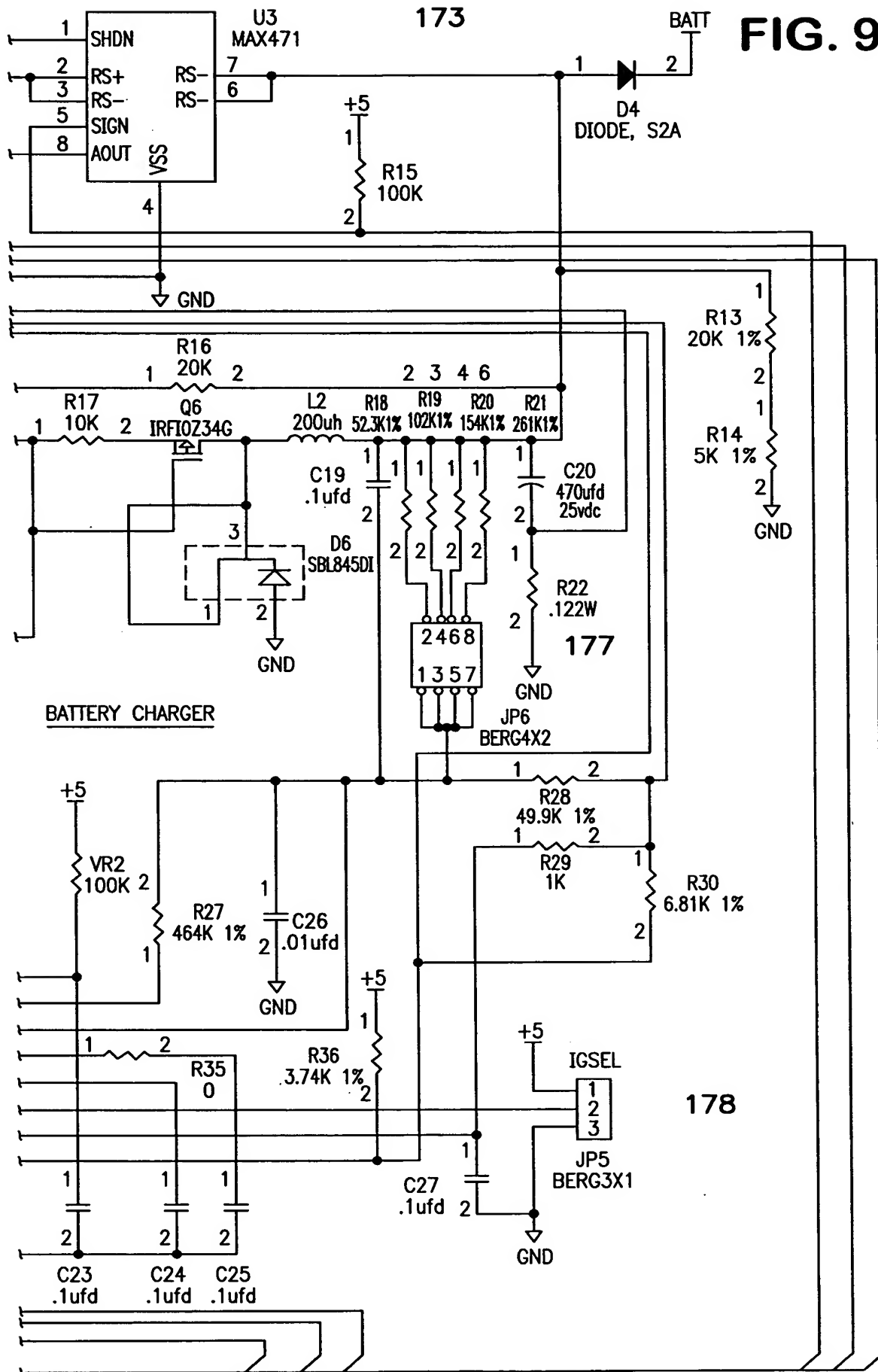


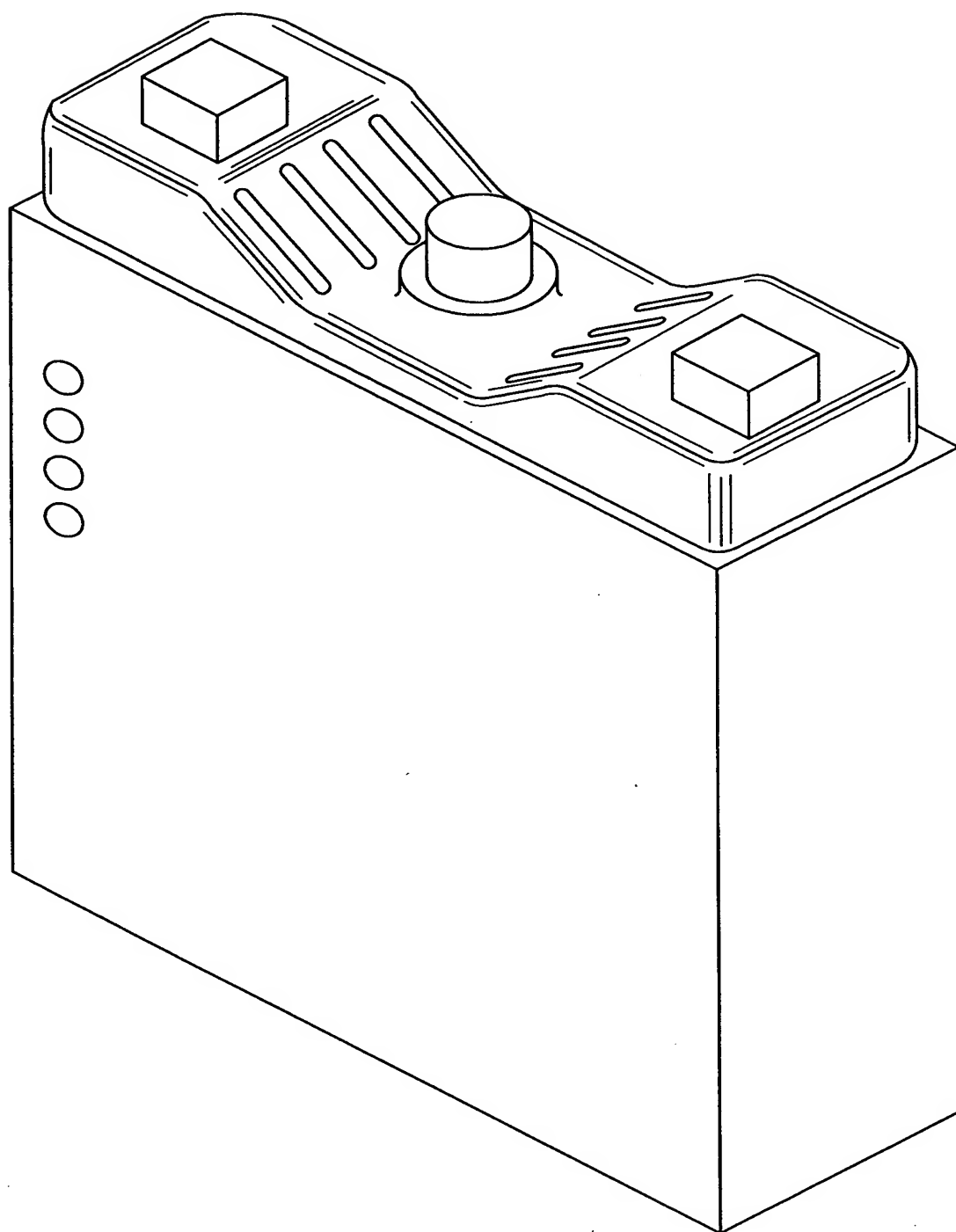
**FIG. 8-2**



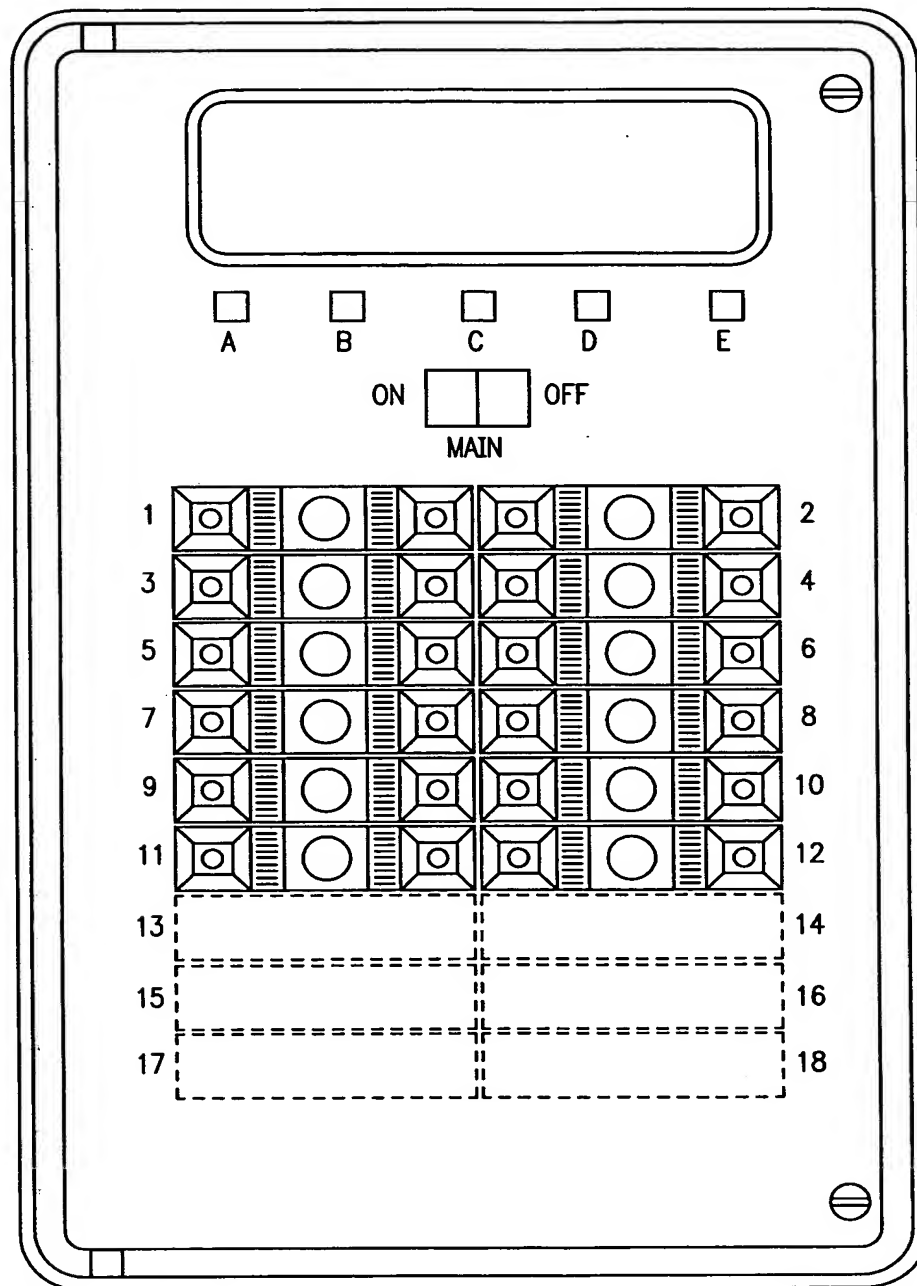
[illegible]

PBus[0..100]





**FIG. 10**



**FIG. 11**